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## Column One

Happy New Year! This seems an appropriate time to say thank you to all of our subscribers and advertisers for their support over the past year and to announce our expectations for the coming year. It is the start of our sixth year and already the year is starting off with a flurry of activity on all fronts.

But first, one bit of old business. Please look back to page 14 of the November issue and find the "Reader Survey". The response has been good, but we would like to hear from every subscriber. This is your chance to get your two cents worth in and to tell us what direction you want PEEK to take in the future.

COMDEX is now over and although there was not a lot of new and exciting innovation this time, it was a show that was good to OSI. Attendance and interest were good and the hints of new and better machines (probably of the 68000 family) just around the corner this spring whetted the appetite of attendees. Best guesses are that these new speedsters will be running UNIX and still take OS-U in stride.

Continuing on the OSI front, word has it that the Source Book (of software) is being completely revamped and updated. You know the Work Systems - the packaged deal? The package just got better. Now it is your choice of extras to go with the 235 and 245 machines. The old 12 bit parallel printers are just about gone and instead you can have a Citch 25 cps if you like. There is also a choice of Esprit terminals (T6110 and T6310), Alloy tape back-up, and P220 DataSouth printers.

You may not have heard it, but there is a board "swap" service plan in effect for dealers to both simplify and expedite repairs. Word has it that the plan may well be expanded to include hard disks too!

Another tidbit, for all users, is that there is a possibility that there might be a price reduction in the cost of OS-U and a return to a reasonable price for "up-grades". We, for one, would like to see "U" competitively priced in the market place and all users using the latest version.

A hardware change has taken place in the 235 line. The 10 MB disk has been replaced with a 13 MB unit<sup>\*</sup> for the same price.

In the meantime, the folks at DBI have not been sitting on their hands either. Sales are reported to be brisk with an ever increasing demand for more users on one machine. The box featured last month is rumored to be spawning a big brother with 16 slots. By my count, that probably means up to 14 users and we understand that there is a good chance that it will support up to a pair of 160MB disks too. Now that's no small time play toy!

In their spare time, we understand that the DBI people are also hot on the trail of a 68000 configuration which is a natural jump from the current DB-DOS operating system. It too, is expected to support a OS-U shell.

Now comes a piece of news you probably wish you were not getting. You have often heard us say that PEEK is a labor of love. Well, it is all too true. But no amount of love will pay the printer much less our beloved Post Office which is poised to raise its rates again in February. We have watched our costs very closely and remain intent upon giving you the greatest amount of information for the least amount of money. We have no intentions of forcing the costs of fancy glossy paper on you, but we started in November to "trim" the edges so that yeu can thumb the pages more easily. We thought this to be desired improvement, although no one seems to have noticed. The last time that PEEK increased its rates was way back in July of 1981. Not much else has gone unchanged that long. So, effective January 1, 1985, the one year sub-scription will be \$19.00 domestic and other rates as indicated in the copyright block on the inside front cover.

So the year gets started with a bang. Now two manufacturers are hard at it, in a friendly way, which prompts a parting thought for "P" users: Did you ever think about what you would have if you stuck a DB-1 or 515 board in your fourslotter?

### 6502 ASSEMBLY LANGUAGE PROGAMMING CLASS

### PART VII

By: Richard L. Trethewey Systems Operator for the OSI SIG on CompuServe

In the last lesson, I described the instructions on the 6502 which perform discreet and familiar mathematical operations, namely addition and subtraction. However, the 6502 instruction set includes additional commands which perform logical and bit manipulative operations that in turn extend the range of mathematical possibilities to the Assembly language programmer.

logical Let's look at the instructions first. There are three such instructions; AND, OR, and EOR (for Exclusive The best way to describe OR). the effect of these instructions is with what are called truth tables.

AND	OR	ÉOR	
0 1	0 1	0 1`	
0 1 0 1 0 1	0 ! 0 ! 1 !	0 ! 0 ! 1 !	
1 ! 0 ! 1 !	111111	1 ! 1 ! 0 !	

I have my own ways of thinking of these instructions that you might find useful. I think of "AND" as a filter; since 1's are only allowed to pass through if they are in the same position as the 1's in the filter. I think of "OR" as a combining command in that all of the l's from both bytes involved are reflected in the result. Finally, I think of "EOR" as an inverter because the l's in the masking byte cause the bits in the sourcebyte to be inverted - if they were 0's they become 1's and if they were 1's they become 0's.

Finally, we have the bit mani-

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pulating instructions of which there are four: ASL, ROL, LSR, and ROR. These commands shift the bits within a memory lo-cation or the accumulator one position to the right or left. As I will illustrate below, these instructions always cause one bit to "drop off" and one bit position within the byte being manipulated to become "open". The difference in the "shifting" and "rotat-ing" instructions is in how the open bit is handled. Both types force the bit that dropped off into the Carry Flag of the Status Register. Let's look at the shift instructions first: ASL

<u>.</u>	 Carry	Flag	
+<=			 0
LSR			

							Y 1		ay	· · ·	_		~~~	,				
																	1	
	+		+-	 -+-		-+-		-+-		+		-+-		-+-		+	i	
0>																		
• •	•	•••	•		•	•		•		•	•	•	-	•	-	•		

ROL

=7====> Carry Flag

•	+<=	1	6	1	5	1	4	1	3	L	2	1	1	1	0	1	х	1	<-+	
		٠	_	+		+		+-		+		+		+		+		+		
										•										
ROR																				

•	+		(==	-	==;	> (	Cai	n	71	71a	ag	40		-	-0-		-		+	
	1				•														1	
	1	+-		+		+		+		-+-		+-		-+-		-+-		+	1	
	+->	1	х	1	7	1	6	1	5	í	4	1	3	1	2	i	1	ì.	=>+	
		-						- 2	-	- 7	- Tr		-		_		_			

You'll note that the numbers in the above tables represent the original position numbers of the bits involved, but they are shown to be in the actual position where they end up after the instruction has been executed. Note also that if you ignore the Carry Flag, the result of movements to the right decrease the value of the byte involved by two and movements to the left increase the value by two. This prin-ciple comes to light many times when indexes to a table of 16-bit values are calculated. The bit manipulative instructions are also useful in routines to multiply and precision divide multiple values.

So far in our discussion, we have dealt exclusively with binary math even though we have often relied on the hexadecimal numbering system. Of course, us poor mortals were all taught only to think in base ten or decimal. Here again, the 6502 comes to our Here rescue. The 6502 has a special mode of operation in which it treats individual bytes as if the high nibble (the 4 most significant bits) were tens

instead of sixteens and it does this only for the instructions ADC and SBC. To enter this mode, we must set the Decimal flag in the Status register with the "SED" in-struction. Since each byte can only hold a 2 digit hexadecimal number, this same byte can only represent the values 0 through 99 when in the deci-mal mode. Let's take a look at a quick example of the decimal mode:

Normal	Mode	Deci	mal	Mode
18			18	
+ 24		+	24	
3C			42	

It is worth noting that in the decimal mode, a byte might easily contain an illegal value. That is to say that whenever either the most or least significant nibble contains a value greater than 9, that value is illegal in the decimal mode and calculations using such illegal values will be erroneous.

The decimal mode is often used in converting binary values into their decimal equivalents. It is also often used to perform accurate floating point calculations to base ten results.

The representation of values in the decimal mode is called Binary Coded Decimal or BCD. The instruction "CLD" clears the Decimal flag and restores normal operation to the 6502.

### BEGINNER'S CORNER

Bv: L. Z. Jankowski Otaio Rd 1, Timaru New Zealand

### SORTING

It's a big subject. Over three dozen sorting algorithms are known. A comprehensive reading list would span over one hundred texts!

Unfortunately, many of these texts are written in 'University English' and deal with such esoterica as 'Sorting with drum storage', not to mention "Amphisbaenic sorting' (something to do with lizards The micro user is usually left bewildered by the range and complexity of sort-ing algorithms. A good intro-duction to the subject would be, 'The Art of Computer Programming, Vol 3 by Knuth. This is not an easy book This is not an easy book.



It is a fact that no one sorting method is 'best'. The Bubble Sort is very slow for a list size of 12 or more, unless there is parallel storage and the members of the list are less than an average of log n positions out of place. Quicksort is fast for random lists with over 500 elements. The Insertion Sort is fastest when sorting one element into an already ordered list. It is evident that sorting algorithms must be matched to specific needs.

When choosing a sorting method several factors must be considered. The two factors which affect the speed of every sorting algorithm are; how ordered the list is to begin with, and its length. As a sorts can be general rule, made more efficient but at the cost of using more computer memory. A list may be re-quired to be sorted without actually moving members of the list. The list could be so large that a merging technique is required. The length and complexity of the sorting pro-gram could also affect sorting speed. The programming lang-uage used and even the hard-ware could have their influence on speed also.

The micro user must chop through this sorting jungle to find the most suitable algorithm. Fortunately, the search can be considerably narrowed by throwing out all Bubble Sorts and their like, and concentrating on short, RAM only sorts. The OML uses two sorting routines, see Listing 1, lines 1270-1460 (Otaio Mailing List, see June '84 issue). They are the Insertion Sort and the Shell Sort.

The Insertion Sort is only four lines long, 1340-1370. The Shell Sort is effectively only 6 lines long, 1400-1450. The Insertion Sort is simplest to understand. Search forward for an out of order element, then a search back through the list and insert the element in its correct position. The Shell Sort is a little more complicated. Elements compared are a specified distance apart. This distance decreases apart. This distance decreases until only adjacent elements are compared. The actual dis-tance follows the sequence of numbers ..., 511, 255, 127, 63, 31, 15, 7, 3, 1 and is calculated in lines 1390 and 1400. The numbers are chosen so that for any number, it and its predecessor, have no common factors.

Strictly speaking, there is no

1 REM LISTING 1 2: 470 REM PACK FILE 480 PRINT "Is the File SORTED ? ";: GOSUB 310: PRINT Y\$: IF A=121 THEN 510 490 IF YS="" OR A=104 THEN 540 500 PRINT : PRINT \*# SORT File first Bub! #\*: PRINT : GDTD 200 510 PRINT !(28): PRINT TAB( 20)\*8 PACKING #\* 520 9=Z 530 IF LEFT\$(D\$(0,1),2)="ZZ" THEN Z=Z-1: FOR Y=1 TO P: D\$(0,Y)="": NEXT : 60TO 520 540 6010 190 550 : 1270 REN SORT FILE 1280 PRINT "SORT on which FIELD # ? ";: GOSUB 310: PRINT Y: B=Y. 1290 PRINT : IF Y=0 OR Y>P THEN 1460 1300 PRINT "Is the File partially Sorted on Field"B" ? "; 1310 GOSUB 310: IF A=104 DR A=45 THEN 1460 1320 PRINT !(2B): PRINT TAB( 20)\*# SORTING #": IF A=110 AND Z)4 THEN 1390 1330 : 1340 FOR 0=2 TO Z: Y=0: FOR C=1 TO P: X\$(C)=D\$(Y,C): NEXT C 1350 IF D\$(Y-1.B) (=X\$(B) THEN 1370 1360 FOR C=1 TO P: D\$(Y,C)=D\$(Y-1,C): NEXT C: Y=Y-1: IF Y>1 THEN 1350 1370 FOR C=1 TO P: D\$(Y,C)=X\$(C): NEXT C,Q: GOTO 1460 1380 : 1390 I=(2^INT(LOG(Z)/LOG(2)))-1 1400 1=INT(1/2): IF 1(1 THEN 1460 1410 FOR Q=1 TO 1: R=Q+1: FOR C=R TO Z STEP I: Y=C 1420 FOR K=1 TO P: X\$(K)=D\$(Y,K): NEXT K 1430 IF D\$(Y-I,B)(=X\$(B) THEN 1450 1440 FOR K=1 TO P: D\$(Y,K)=D\$(Y-1,K): NEXT K: Y=Y-1: IF Y)I THEN 1430 1450 FOR K=1 TO P: D\$(Y,K)=1\$(K): NEXT K: NEXT C,8: 60TO 1400 1460 GOTO 190 1470 : **1480 PRINT FILE** 1490 INPUT \*\* \* of copies of each Record \*;L: PRINT : IF L<1 THEN 1680 1500 : 1510 SS=0: PRINT : PRINT "Two columns ? ";: GOSUB 310: PRINT YS 1520 IF A=110 OR A=45 THEN 1570 1530 IF A=104 THEN PRINT : 60T0 1490 1540 PRINT : PRINT "Records must be PACKED. Hit (RETURN) if not." 1550 SS=-1: E=ST+1: TB=40 1560 : 1570 PRINT : PRINT \*Device # ? \*;: 60SUB 310: IF Y=0 THEN 1680 1580 PRINT Y: V=Y: PRINT : PRINT "Ready ? ";: GDSUB 310: PRINT : PRINT 1590 FOR Q=1 TO Z: FOR X=1 TO L: IF LEFTS(DS(Q,1),2)="ZZ" THEN 1670 1600 : 1610 IF SS=0 THEN 1660 1620 IF Q=E THEN E=Q+ST#2: Q=Q+ST: PRINT #V,F\$ 1630 IF 937 THEN 1680 1640 GOSUB 1920: GOTO 1670 1650 : 1660 GOSUB 1880 1670 NEXT X.0 1680 V=2: 60T0 190 1690 : 1870 REM Print a Record 1880 PRINT #V, D\$ (0,1) TAB ( 32) 0: FOR C=2 TO P: PRINT #V, D\$ (0,C): NEXT C 1890 PRINT #V: RETURN 1900 : 1910 REM Print a Record in 2 columns 1920 PRINT #V.D\*(0.1)TAB( 32)@ TAB( TB)D\*(0+ST.1)TAB( TB+32)@+ST 1930 FOR C=2 TO P: PRINT #V, D\$(8,C) TAB( TB)D\$(8+ST,C): NEXT C: PRINT #V 1940 RETURN

such thing as a 'Shell Sort'. The algorithm can be applied to any 'compare and exchange' algorithm, and is here applied to the 'Insertion Sort'. The Shell Sort is best for a random list with at least 5 elements. The Insertion Sort is best for a list with only a few out of order elements.

Looking at Listing 1 again, notice that, because an array is being used to store the list, it is easy to pin-point the field on which the list is to be sorted - see lines 1300, 1350 and 1430. During the sort, when records need to be swapped, every field of every record is swapped. This is time-consuming. The sort would run considerably faster if only an index to the list had to be sorted. However, this extra speed has a cost. More computer memory is used and the program has to be longer and more complex to cope with the insertion and deletion of records.

When choosing a sorting algorithm, the wise computerist tests it with actual data before making a final choice. Text books on the subject are best treated as guides only.

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For many applications two sorting programs are required if speed is important. One program would serve random lists and the other would be for nearly-ordered lists.

### PRINTING

This part of the OML is complicated by the option on twocolumn printout. The option was written in to provide a tidy two-column printout of databases which are not a mailing list. This option is flagged by the variable 'SS'. The number of records per column is held in 'ST' (see line 130). Line 1620 sends a formfeed to the printer after a page of records has been printed in two column format. Notice that in line 1610, before each record is printed, the 'SS' flag is checked. This is because the single and double column printout rou-tines are mixed together. For program clarity, these two routines should be separate. The cost of doing this is a longer, slightly slower program.

#### PACKING

Packing means removing some-thing unwanted from a file. For example, a BASIC program can be packed by removing all blanks and REM statements. A file of records is packed when deleted records are removed from the file. In the OML the process has three stages. First, a record is 'Deleted' from the (but not removed) file by writing 'ZZ' as the first two characters of the first field. This is useful for subscription lists. Late payers are not sent the latest mailing but remain in the file for quick 'undeletion' as soon as their sub is received. If the sub is not received the record can be removed from the file by the next pair of actions. First, records are 'Sorted' on field 1 and, Hey Presto, all deleted records appear at the end of the file cunning those 'ZZ's'! Now 'Packing' the file merely re-moves all those records marked with a 'ZZ'. The file is tidy once again.

### WAZZAT CORNER!

By: L. Z. Jankowski Otaio Rd l Timaru New Zealand

### PRINT USING IN BASIC

A PRINT USING command is used

to format numbers on the decimal point. In some BASICs the command can be used to format graphics. Very few versions of BASIC have a 'PRINT USING' command. OSI implemented the command in OS65D 3.3, and it is particularly easy to use. Obviously, a machine version of PRINT USING code is preferable to a BASIC program equivalent if only because equivalent if only because string garbage collect is avoided. But a BASIC program can still be very effective, particularly if there is no other choice! Such a program is presented here.

The subroutine that does all the work is in lines 190 to 240 of the program. Numbers are right-justified on the decimal point. This can be done anywhere on a line as set by the value of 'T'. Leading and trailing zeros are correctly printed, no matter how the number is input in line 40. The sign of the number is also correctly formatted. Line 190 could obviously be made an earlier line, for example line 20.

The most efficient way to manipulate numbers in BASIC is with string functions. The first step then, in line 200, is to convert the number 'N' into string 'M\$'. Multiplying 'N' by 100 guarantees trailing zeros, if they are there. Next, the sign of the number is copied into 'S\$'. In line 210 the sign is deleted from 'M\$'. Leading zeros are then attached, if required. Fin-ally, in line 230, the number is printed, right justified on the decimal point. Line 240 forces a very brief garbage collect. Doing this removes the need for BASIC to do a lengthy garbage collect when string space becomes full. Several numbers could be formatted on one line by changing the value of 'T' as required. Also, a semi-colon would need to be placed at the end of line 230 and a 'PRINT' done after the printing of each line of formatted numbers.

Listing 2 illustrates a good programming practice - writing good programs which are as general as possible, with input-error trapping. This 'print-using' program gives the user a choice on how many digits are to follow the decimal point. If the program is to be run in ROM BASIC a change is required in line 50. Change the number to 1 000 000, ROM BASIC arithmetic is limited to 6 significant figures. The tabbing variable is now 'P', in line

### See listings page 23

### BASIC RULES OF RESPONSIBLE PROGRAMMING

By: Luis E. Robles P. O. Box 2036 Hato Rey, P.R. 00919

First of all, I would like to commend you on your excellent newsletter to which we have been subscribing since day Throughout the years it one. has proved to be very useful and enlightening, and most of all consistent. CONGRATULA-TIONS!

Puerto Rico Computer, is one of the few remaining "old "old guarde" distributors. We have been around since OS65U ver-sion 1.1, devoting ourselves mostly to the selling, programming, maintenance and in-stalling of commercial applications. Besides the bread and butter applications of Billing, Inventory, Accounts Receivable, Accounts Payable, Sales Analysis, General Led-ger and Payroll, we have developed also such exoteric applications as Insurance Premium Financing, Insurance Agency Accounting, Custom Brokers, Wholesale Lab., Test Bill-Leasing Companies and ing, Time Management Accounting and Invoicing.

In our quest of many years for the painless and easy to maintain software, our experience with packages has been nightmarish (!) to say the least, making us retrieve our old faithful software more times (and money) than I want to remember. Finally, we to remember. Finally, we reached the conclusion that we must have been doing something right, and that the grass was indeed greener on our side. We have been mis-led by well advertised, SOME-TIMES WELL DOCUMENTED, software packages, that lack what we call the basic rules of 'responsible programming".

We have divided our notes in six main topics which we feel are the keys of our success in the software environment. These are:

- 1. File Structure and Sorting
- 2. Query & Creating Programs
- 3. Listing Programs
- 4. Updating Programs
- 5. Merges
- 6. Menus

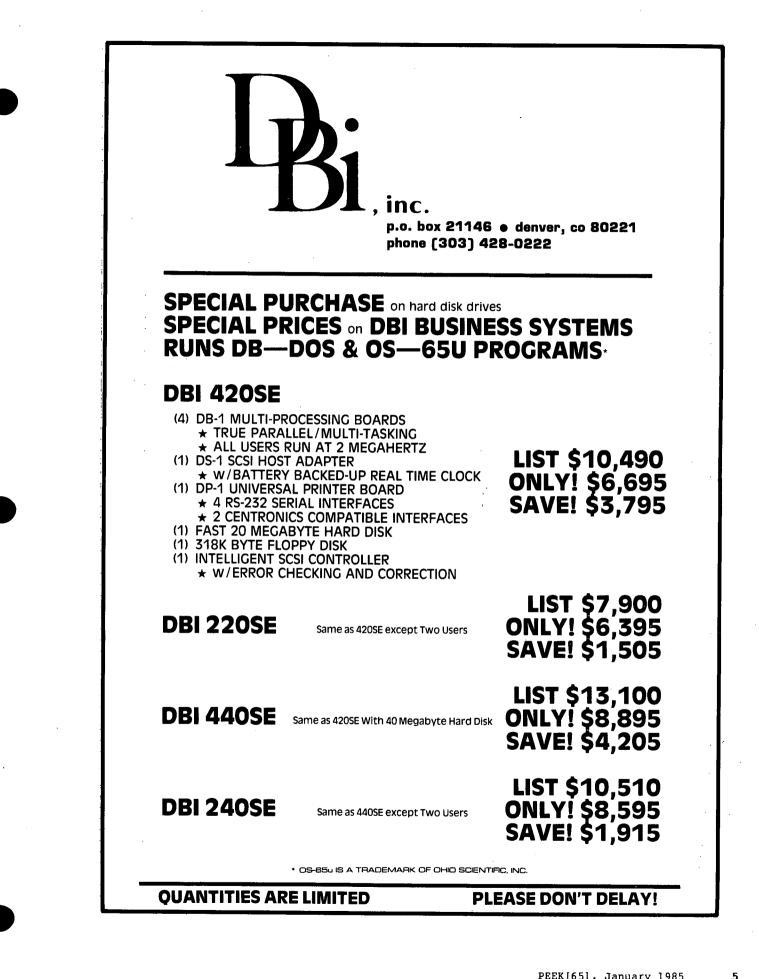
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FILE STRUCTURE AND SORTING

### First of all, we decided to

PEEK[65], January 1985





stick to OSI's DMS standards, with a few changes. The only requisite to understand what follows is to be familiar with OSI DMS file structure.

We concluded that there were basically three field types, which are: string, numeric, and date. So we added an S, N, and a D to the label of each field which, for convenience, we made 12 characters long (please, keep in mind these were prehistoric times before the Extended Input Capability).

On the issue of sorting we decided that we were going to sort only key files and leave the master files untouched which turned out to be a wise decision in an era of brownouts and power failures. But instead of using type 11 key files format, the DMS standard, we left it as a type ten with one field. Then the sorting order was stringed or concatenated. For example: If we wanted to sort a file by credit limit and customer number, we constructed a key file as follows:

```
01 +
(CREDIT LIMIT)
```

23456 + (CUSTOMER NUMBER)

12345678 + (DISK ADDRESS, ALWAYS 8 CHAR-ACTERS LONG)

KEY = 012345612345678

QUERY AND CREATING PROGRAMS

Our next logical step was to create a standard program that could handle these modified DMS files, and, by using S, N, and D indicators, edit the fields as the operator was entering them. So as soon as you have created the file, with DBMFCR (remember?) we could immediately provide the various options of Creating and Query, Listings, Updating, Merging, and Menus programs. Probably you are asking, "Why not use the DMS package developed by OSI?" Well, we felt these programs were good tools but needed the editing, previously mentioned (S, N, and D) and also some additional controls to work properly in an office environment, these were:

1. The first field was always going to be a string field and the "key" by which to make sure the record in question was going to be uniquely identified, i.e., Customer number, Inventory number, etc., or a control number generated by the program, if we could not identified the record "uniquely".

2. By using this first unique field whenever we are creating a record, the duplication of customers number, for instance, could be avoided by making a "find" before proceeding with the other fields.

3. Also, by using this first field, whenever quering, we could search the file and if the record already has been created, display the other fields of the record, or indicate that it doesn't exist.

4. Always make sure fields are of the proper size by padding it with right hand spaces. Also, always write in a record boundary (see formula below).

	/-	INDEX(1) - BODF -/	
INT	1	/	=
	1-	RECORD LENGTH -/	

INDEX (1) - BODF RECORD LENGTH

5. Use the "^" sign to precede the first field (the key). This will simplify the find (nevertheless, check for record boundary).

6. To erase a record, substitute the "^" with a "i". By the way, do it by programming, by no means leave it to the operator.

7. By using the "I" for erased records you could easily recover them in case of emergency.

8. Whenever the file gets full (EODF=BODF), search for the erased records by search-ing for the "I" and reuse the space.

9. All numeric fields are going to be nine characters long. Why this waste? Well as you know, OSI is hot into hard disks so. if you recommended the proper hard disk size to your customer, no problem with volume. Therefore, by defining numeric fields with a length of nine, you will be sure to reach a field overflow only when reaching OSI's maximum number handling capacity. Also you will avoid the problem that so many many packages have, of finding out you are overflowing a field at the time you are updating it. What to do then? Redefine the file, erase the old one, run a packer, and then return to the exact point where you were updating (whew!).

What then, if by using our method, you overflow a nine character long field? Truncate and give a message (a very common occurrence in the construction and leasing industries where you deal with large figures).

10. Following we will mention several screen formats or variations of the query/ create programs as we have modified them. These are:

a. Standard Query/Create, first field is the Key. Labels are posted left hand side of screen.

b. Standard Query/Create, first field is the Key. Labels are posted in two columns: Notice customer name is left on a line by itself since usually this second field (or item description) will overlap the right hand column (usually over 30 characters long).

c. Multi Screen- more than one screen

d. Batch control- first field is a control number generated by the computer to uniquely identify the record.

e. Order entry- The screen is divided into top half and bottom half. The top half includes the invoice No., customer No., date, salesman, ship to, sold to address, and "terms" line. The bottom half includes the item No. description, amount order, back order, unit price, price extension, discount, total amount, etc.

### LISTING PROGRAMS

Basically you need four types of Listing Programs:

1. Dumping a file as it is.

2. Dumping a file by first referring to a sorted Key file. It is usually requested to break at totals by category (usually not more than two subtotals, i.e., subtotals by salesman, by item, and final totals).

3. Aging and Statement printing. The previous program (No. 2) could be used as the base to build these, though the key file will have to "merge" two masters files, (customer and transactions), and keep track of which file

the key points to.

4. Invoice printing: This printing program also merges two masters; the sold to, ship to, and billing information with the detail or item lines file. The aging and statement program could be the basis of this invoice printing program.

We developed a utility that works nicely to run fast, printing programs Types 1 and 2 (above). We eliminated the instructions that print the heading and the variables from programs 1 and 2. Then we made a program that asks you, one by one, each field heading and variable name. Convert these inputs to printing instructions of heading and variables. Then "list" it to a file and merge these heading and variable names (Flags 13 and 14) with the printing programs.

### UPDATING PROGRAMS

There are three basic updating programs.

1. Adding transaction to a file, i.e., you want to merge a batch of payments with a transaction master file. First make sure you have enough room to accommodate the new transactions by testing the master file EODF. Second, after adding the transactions, don't forget to "Erase" the temporary file.

INDEX<1>=9: Print % 1, EODF

Try to do this EODF updating right at the end of the program to minimize a call to the programmer. In other words, if the update program is interrupted for any reason, most probably just by running it again everything will be Ok. After updating the EODF, always post the words "accumulated" and amount at the end of the batch listing. This will save you a lot of guessing if anything goes wrong.

2. Eliminating transactions from a permanent file - If for example the "permanent" transactions file needs to be cleaned up of all accounts that balance to zero, we have found out that the best way is to create a twin or image file and pass the transactions that DON'T zero balance. Then when finished interchange the file names with a modified Rename program. By the way - you will need intermediate file names to avoid hanging up in a Duplicate File name error. By using this technique you get a bonus. You have an internal backup of last month's transactions file, and this alone certainly can save the day!

3. Updating totals in a master file - As an example, quarter to date and/or year to date figures in a payroll master file. As in the previous example, create a twin or image file and copy the payroll master file to it, write a flag in the control file stating that an update is in process then proceed to update the master file. If the update is not succesfully completed "lock the system" and recopy the master file from the image file. Then proceed with the update again. This technique saves a lot of time when the update is interrupted since it avoids the painful process reconstructions and balancing totals. Also, as before, you get a bonus since you have an internal back up of the master file previous to the update.

### MERGES

A simple program that either writes and/or merges "key files". Avoid writing on a "one by one" basis, i.e., read a master file record and write a key file record. That really ties up the system. Instead, dimension variables to the maximum. I feel a good guess is 3584 divided by the record length, i.e., 3584/10 = 358:DIM A\$(358). Gobble up all the master records you can from one read. Construct the keys using dimensionable variables and write all the keys in one burst. You will be surprised by the speed!

### MENUS

Last but not least, remember most of the time the menu is what sells. Make it look attractive and functional (though this is very subjective) avoid displaying all Query; programs together and all the listing programs together and the same applies to the Sorting programs. Even though this looks neat it tends to confuse the operator since he has to look all over the screen to find the different related program.

OS COMPUTER &	PARTS	GLOSE-OUT!	mon
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(For disk based systems)

By: David Livesay Ave de la Resistance 6 b4920 Embourg, Belgium

One of the well known problems with Microsoft BASIC is that when the interpreter finds a GOTO or GOSUB statement it starts to search for the correct line and in some cases must look at each line in the program. For example, if we have a 200 line program and at line 1 we find a GOTO 200 statement, the BASIC interpreter will search through 199 lines to find line number 200. If on line 100 we have a GOTO 1, the interpreter will look at line 101 and discover that it is greater than the line number that is being searched for. Whenever this occurs, the search goes back to the beginning of the program. The interpreter will have looked at 2 lines to find line 1. Obviously, the placement of the subroutines can make a big difference in the run time of a program. There have been many articles published that suggest putting the subrou-tines at the beginning of the program to speed up your programs. The following de-scribes a technique for speeding up the search for line number routine in the BASIC interpreter.

In connection with their work on interfacing a Motorola 68000 to the Apple computer, the people at Digital Acoustics developed a solution to this problem. The solution that I show here is an adaptation that I developed while working on interfacing the Digital Acoustics 68000 board to the OSI.

The solution to this "search for line number" problem is to replace the normal search routine with one that looks up the address of the correct line in a table. The following must be done:

 Space for a table must be provided, - this must be two bytes for each line of the program.

2. The program must be numbered, starting with 0 with steps of 2, i.e., 0, 2, 4, 6, 8 ...

3. Before the first GOTO or COSUR is encountered, the BASIC program must call а machine language program that builds the table of addresses. This program also checks that MEMORY DUMPS

 D
 1
 2
 3
 4
 5
 6
 7
 8
 7
 8
 B
 C
 D

 0
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 CF
 3B
 D5
 3B
 D8
 3B
 E1
 3B
 E7
 3B
 D5
 3C
 D8
 3B
 E1
 3B
 E7
 3B
 D5
 3C
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 3D
 11
 3C
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 11
 3C
 17
 3D
 10
 3C

 2F
 3C
 35
 3C
 3D
 3D
 2T
 3C
 4D
 3C</ C9 38 F9 38 29 3C 59 3C 89 3C 89 3C E9 3C 19 3D 23 30 53 30 83 30 83 30 E3 30 **BA30** BA40 8650 BA60 8A7Ú. BABO 13 3D BA90 00 00

DEMO

0 LM=255:HM=185:REM \$B9FF:REM FOR 48K SYSTEM 2 POKE133,HM:POKE132,LM:POKE129,HM:POKE128,LM:REM PROTECT MEM 4 DISK!"CA BF00=49,1":POKE574,120:POKE575,191:REM \$BF78 6 X=USR(X):INPUT"DO YOU WANT TO USE TABLE FOR GOTO":A\$ 8 IF LEFT\$(A\$,1)="Y" THEN POKE 49145,0:GOTO12:REM \$BFF9=49145 10 POKE 49145,1 12 FORI=1T03000:60T0150 14 NEXTI 16 END 18 607014 20 REM LINES 20-146 ARE ALL THE SAME 146 REM 148 REM

#### ASSEMBLY LISTING

150 GOT014

10 0000	; SEARCH	FOR LINE# PROGR	AM
20 0000	; FOR OS-65D		
30 BF60	*≈\$BF60		
40 BF60	POKER=\$19	:TEMP PNTR FOR LINE# TO BE	FOUND
50 BF60	BPTR≠\$C7	BASIC PNTR CURRENT PROGRAM	ADDRES
60 BF60	SOB=\$78	START OF BASIC PROGRAM	
70 BF60	UPA=\$E3	UTILITY POINTER A	Continued

BO BEAO UP8≈\$F5 UTILITY POINTER B UTILITY POINTER C UTILITY POINTER C TEMP LINE# COUNTER TO BUILD TABLE HIGH BYTE FOR TABLE ADDRESS LOCATION FOR SYNTAX ERR ROUTINE 90 BF40 100 BF40 UPC=\$F7 BLIM=\$BFFE 110 BEAD TBI = \$ PA 120 BF60 SYNERR=\$0E1E 130 BEA0 140 BF60 ;\*\*\*\*\*SEARCH BASIC FOR A GIVEN LINE#\*\*\*\*\*\*\*\*\*\*\*\* 150 BF60 160 BF60 170 BF60 180 BEA0 A5 19 ROKER \*\*\*\*\* 190 BF62 85 F7 STA UPC SET UPC TO POINT TO THE 200 BE64 1B CL C 210 BF65 A5 1A 220 BF67 69 BA LDA ADC POKER+1 :ADDRESS OF THE LINE# #TBL UPC+1 230 BE69 85 E8 STA \*\*\*\*\*\*\*\*\*\*\*\*\*\* 240 BF6B A0 00 250 BF6D B1 F7 #\$00 (UPC),Y \*\*\*\* LDY LDA 260 BF6F 85 C7 FETCH ADDRESS TO BETR STA BPTR 270 BF71 C8 280 BF72 B1 F7 290 BF74 85 C8 INY (UPC).Y LDA STA SPTR+1 300 BF76 38 310 BF77 60 INDICATES ADDRESS FOUND RTS : RETURN 320 BE78 : 330 BF78 340 BF78 350 BF78 360 BF78 370 BE78 380 BF78 390 BF78 A9 00 BUILD LDA #\$00 \*\*\*\*\*\*\*\*\*\*\* 400 BF7A BD FE BF 410 BF7D BD FF BF SET 1ST LINE# = 0 STA BLIM BLIM+1 STA \*\*\*\*\* 420 BF80 85 F5 430 BF82 A9 BA STA UPB Ur B #TBL L.DA SET ADDR TABLE PTR = TBL \*\*\*\*\*\* 440 BE84 85 E6 STA IIPB+1450 BF86 A5 78 460 BF88 B5 E3 470 BF8A A5 79 LDA SOB UPA SET POINTER UPA TO THE START OF BASIC LDA SOB+1 480 BF8C 85 E4 490 BF8E STA UPA+1 \*\*\*\*PLACE JMP INSTRUCTION IN BASIC\*\*\* SOO BEBE 510 BF8E \*\*\*\*JMP TO TEST FOR USE TABLE FLAG\*\*\* 520 BE8E . 520 BF8E A9 4C 530 BF8E A9 4C 540 BF90 8D A9 08 550 BF93 A9 EB 560 BF93 8D AA 08 570 BF98 A9 BF ιna 44.40 STORE JMP INSTRUCTION \$08A7 STA LDA #\$EB \$08AA STORE LOW ADDRESS LDA #\$EF 580 BF9A 8D AB 08 STA \$08AB STORE HIGH ADDRESS : 600 BF90 610 BF9D 620 BF9D \*\*\*REPEAT THIS LOOP FOR EVERY BASIC LINE#\*\*\* 620 BF9D 630 BF9D 640 BF9D A0 01 650 BF9F B1 E3 660 BFA1 F0 44 670 BFA3 C8 BILD1 LDY :PTR TO HI LINK BYTE :FETCH THE HIGH LINK BYTE :DONE IF ZERO :\*\*\*\*\*\*\*\* #\$01 LDA (UPA), Y BEQ GDONE INY 680 BFA4 B1 E3 690 BFA6 CD FE BF 700 BFA9 D0 08 (UPA) Y LDA CMP BLIM :CHECK WHETHER THE LINE # :IS IN CORRECT SEQUENCE BNE ERR INY 710 BEAB CB 720 BFAC B1 E3 730 BFAE CD FF BF (UPA) Y BLIM+1 OK SYNERR CMP 740 BFB1 F0 03 750 BFB3 4C 1E 0E ERR BEQ SYNTAX ERROR ON WRONG #SEQ JMP 760 BFB6 A0 00 ÐΚ LDY #\$00 \*\*\*\*\*\*\*\*\*\*\* 770 BFBB A6 E4 780 BFBA 38 UPA+1 LDX SEC STORE THE ADDRESS OF THE BASIC LINE # AFTER SUBTRACTING DNE FOR LATER USE WITH "GETCHR" 790 BEBB AS ES LDA LIPA 800 BFBD E9 01 810 BFBF 91 F5 SBC #\$01 (UPB),Y STA 820 BFC1 B0 01 830 BFC3 CA BCS AA DEX 840 BFC4 C8 AA INY TXA 850 BFC5 8A 860 BFC6 91 F5 STA (UPB) Y \*\*\*\*\* 870 BFC8 88 880 BFC9 18 :SET Y =0 DEY CLC 890 BECA A5 E5 I DA UPB 900 BFCC 69 02 ADC #\$02 910 BFCE 85 F5 :ADD #2 TO THE TABLE POINTER STA UPB 920 BFD0 BD FE BF 930 BFD3 D0 05 STA BLIM :UPB AND TO THE REF LINE# BNE BILDA 940 BFD5 E6 F6 950 BFD7 EE FF BF UPB+1 BLIM+1 INC INC \*\*\*\*\*\*\*\*\*\*\*\*\* 960 BFDA B1 E3 970 BFDC AA BILDA LDA TAX (UPA),Y \*\*\*\*\*\* :FETCH THE LINK (PTR TO NEXT :BASIC LIN), STORE IN UPA 980 BFDD C8 INY 990 BFDE B1 E3 1000 BFE0 85 E4 LDA (UPA),Y UPA+1 STA 1010 BFE2 86 E3 1020 BFE4 4C 9D BF 1030 BFE7 EE FE BF STX JMP GDONE INC UPA BILDI :NEXT LINE# :INCREMENT THE LIMIT BY ONE BLIM 1040 BFEA 60 RTS : DONE; RETURN TO BASIC 1040 BFEB 1050 BFEB 1060 BFEB 1070 BFEB 1080 BFEB 1090 BFEB 1100 BFEB 1110 BFEB AD F9 BF 1120 BFEE F0 06 TEST LDA FLAG : TEST FOR USE TABLE FLAG JGOTO BEQ 1130 BFF0 20 0A 09 1140 BFF3 4C AC 08 JSR JMP \$070A \$08AC :FLAG NOT SET RETURN BASIC JGOTO JMP :USE TABLE 1150 BFF6 4C 60 BF GOTO 1160 BFF9 EA FLAG NOP

the lines are correctly numbered and reports an error if the line numbers are not correct. After the table is built, the first two bytes will contain the starting address of line number 0 (actually the address minus one), the second two bytes will contain the address of line number two. etc... 4. A second machine language routine is needed to find the address of the line whenever either a GOSUB or GOTO is encountered. In order to call this subroutine, three bytes of code are changed in the BASIC interpreter. When the new code is called, the rou-tine first checks a flag to determine if the table should be used. If the flag is set,

the routine will return to normal BASIC. IF the flag is not set, the routine will fetch the address of the correct line from the table, and then return to BASIC to begin executing the correct line number.

The principle benefit of the above technique is that long programs can run much faster. The other benefit is that the program can be organized anyway the programmer desires without affecting the speed at which the program runs.

### THE NEW SEARCH FOR LINE NUMBER PROGRAM

In order to understand how the new search for line number program works, we need to understand the structure of a line of BASIC. Each line of BASIC has the following structure:

PP PP	nn nn	bb bb	00
NEXT LINE (LO BYTE, HI BYTE)	LINE #	BASIC CODE (TOKENS AND ASCII)	NULL

If we look at the memory dump of the "DEMO" program, we find in line No. 0, the first address of the next line number at \$3A7F. This address is \$3AAC. At \$3AAC we find the second address which is \$3ADE, etc.. In this way, there is a link from one line to the next.

The routine in the Assembly listing from lines 390 to 1040 is the routine that creates the table. This is the routine that must be called from the BASIC program. In addition to building the address table, this routine also changes the BASIC code at the GOTO function so that anytime a GOTO is found (GOSUB also uses the GOTO function), the BASIC interpreter will jump to the TEST FOR FLAG routine of this program at line 1110 (this will be discussed later). If you look at the memory dump of locations \$BA00 to \$BAA0, you will see the table of addresses that the build table routine creates. Remember that the addresses in the table are always one less than the actual address of the line (BASIC likes it that way).

After the table is built and the program is run, anytime a GOTO is found, the BASIC interpreter will cause a jump to the "TEST FOR USE TABLE" routine. Lines 1110 and 1120 check the flag byte to determine if the table should be used to perform the search for line number function. If the byte has a value of zero, the program jumps to the new routine at line 180 to find the address in the table. If the flag byte is not zero, then the program returns to BASIC to find the line number. Note that the code at lines 1130 and 1140 duplicate the code starting at \$08A9 in the This BASIC interpreter. is necessary because we modified the code at this location to cause the jump to this new machine code program.

The code that finds the address in the table is actually very short. Starting at line 180, the low byte of the line to be searched for is loaded from \$19 (where BASIC has placed it) into utility pointer C (UPC). Lines 210 thru 230 load the high byte and add it to the high byte of the table address. Now what does this do for us? For example, if we are looking for line #2, we find that line 180 loads a \$02 and places it in UPC, then it will load \$00 and add it to the high byte of the table address (\$BA) and stores it in UPC+1. We now have \$BA in UPC+1 and \$02 in UPC, this you will notice is the location in the table for the address of We then load #00 line #2. into the Y register and then use UPC to fetch the low and the high byte and store them in the BASIC pointer for the current line address. We then return to BASIC and the execution of the program continues as before until a new GOTO is found.

The next listing is of a BASIC program called "DEMO". This program simulates a program where a GOTO is 68 lines away and the GOTO is executed 3000 times. Line 2 loads the machine language program and line 4 changes the upper limit of BASIC and sets up the address for the USR(X) function. The POKEs in lines 8 and 10 either set or clear the use table flag. Obviously, the POKE values used in lines 2, 4, 8, and 10 need to be adjusted for the location of your machine code program.

If you run this program and don't use the table, it will run in 27 seconds. If you use the look-up table, it will run in 9.4 seconds. If you change the GOTO 150 in line 12 to GOTO 18 to simulate a situation where the jump location is close, the program will run in 12.6 seconds. From this it can be seen that using this look-up table program can improve the execution times even of programs that are written with the subroutines at the beginning.

If you load and run another program, you must remember to POKE the load flag with a value other than zero, otherwise, the new program will not run. Also, if you make a change to the program, you will need to rebuild the table. If on the other hand, you don't change the program by entering at a location after the build table routine is called. For example, if you want to restart the "DEMO" program, you can restart at line 12.

This routine can be used to speed up long programs that have many subroutines and/or branches frequently. For any application where memory space is critical, you should set up the table space to minimize the usage of memory. Although I have not tried this with OS65U, there is no reason why this technique would not work. Due to a bad memory board, I was limited to 40K when I wrote it, and thus I placed the table at \$BA00 and the machine language program at \$BF60. This provides space in the table for the line addresses of a 688 line program. This can, of course, be changed for shorter or longer programs if desired.

THE NEW OSI PERSONAL COMPUTER

With the advent of new genera-

tion processors capable of ad-

dressing over one Megabyte of

(mavbe)

By: Norman J. Thorsen

22225 Woodward Way NW

Poulsbo, WA 98370

RAM and using sixteen bits of data, it is now time for OSI users to investigate likely replacements for the aged 6502. One likely candidate is the Western Design Center's W65SC816, hereafter referred to as the 65816.

A full sixteen-bit micropro-cessor, the 65816 has 24-bit addressing, 16-bit registers, more op-codes than the 6502 and is fully capable of 6502 In fact, it emulation. is pinand signal-compatible with the 6502, and therefore, could be plugged directly into the 6502 socket. With this much going for it, let's take a more detailed look at this candidate for a new OSI computer.

Figure 1 shows the registers in the 65816 programming model. Close inspection show you that the 6502 will reaisters are still there, only extended for 16-bit operation, one register not present on a 6502 (the Direct register), and two 8-bit Bank registers. Also, note the Address register is now 24-bit. The Status register has been revised to use all bits, with two of them used differently; Memory Select was added, bit 4 is Index Register Select in 16-bit mode and Break in 6502-mode, Carry now also monitors Emulation mode, but is conceptually separate. All registers retain their names except the Accumulator. Each half of each register is distinguished by the letter 'H' or 'L' for highand low-order bytes. The Ī6bit Accumulator is identified as 'C' and the high- and low-order bytes by 'B' and 'A'.

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The Status register illus-

PEEK[65], January 1985

trates the major changes beyond the register extensions. The 'X' flag (Break), a software interrupt in 6502 emulation, transfers control to the same routine as the hardware interrupt. However, in 65816 mode the vector is different. This gives programmers more latitude in interrupt handling since the vector now identifies what type of interrupt invoked it. Software won't have to determine whether hardware or software problems caused the interrupt which should speed up the handling of interrupts.

The 'M' flag (Memory Select) works in unison with 'X' to determine whether index and Accumulator registers are treated as 8- or 16-bit. When the flags are set to 'l', the registers are treated as 8bit; when set to 'Ø', registers are 16-bit. 'M' affects only the Accumulator, 'X' affects the indexes.

The 'E' flag is difficult to explain. It does not exist-in the Status register; it exists behind the Status register 'C' flag and can be exchanged with 'C'. It cannot be directly tested, set or cleared; it must be exchanged with 'C'.

The added register, the Direct register, holds the address of the 'direct' or 'Zero' page of memory. Programmers are not limited to a 256-byte page at the beginning of memory, anymore; 'zero page' can now be anywhere and doesn't have to begin on a page boundary. However, an extra cycle of processor time is needed if the low order byte of the address isn't zero, to add the offset; if it is zero, special circuitry enables the procesor to skip the extra cycle.

New instructions include PHX, PHY, PLX and PLY for stack operations on index registers; PHB and PLB for data bank register; PHD and PLD for direct register; PHK for program bank register (no instruction for pulling the program bank reg-ister). There are new flag handlers, REP and SEP (reset and set status register flags) and TRB (test and reset bits). A new stack-relative addressing mode is available along with new stack instructions. One of these is PEA (push effective address) which pushes the third and second bytes of the instruction, in that order, onto the stack.

There are other instructions added to the 6502 instruction set, too many to go into very

8 bits	8 bits	8 bits
Data Bank Register DBR	X Register High XH - C	X Register Low X) XL
Data Bank Register OBR	Y Register High YH <sup>(1</sup>	Y Register Low Y) YL
00	Stack Register High SH (	Stack Register Low S) SL
	Accumulator B (	Accumulator C) A
Program Bank Reg PBR	Program PCH (F	Counter C) PCL
00	Direct Register Hi	Direct Register Log

much detail here. Most deal with 16-bit operation, some utilize the new addressing modes. The implementation of 16-bit operation in the 6502 world appears to be very well done; new registers, extended registers, new addressing modes and new instructions as well as full 6502 emulation without sacrifice of speed (extended addressing uses an unused half cycle of bus time).

Without going into any more detail on this processor, let's imagine a few things. High-density memory device prices have come down to fairly low levels in recent months. The board size of an OSI computer, using 4164 high-speed Dynamic RAM chips, could probably hold 512 Kilobytes of directly addressable memory. Disk drives, double-sided/ double-density, are not too expensive anymore. High-resolution grahics boards and 80column video board designs are available. The OSI C4P backplane with four slots could be configured with 1 Megabyte of RAM, two double-sided/double-density disk drives and hires/80-column video. The major problem facing OSI computers is a viable operating system written for the 65816 to take advantage of the 16bit operation and speed. There are engineers out there capable of designing the hardware; are there any systems software people reading this?

### $\star$

### Ky. ASM V1.1 ASSEMBLER A REVIEW

By: Damon F. Curry SofTouch, Inc. P.O. Box 31335 Dayton, OH 45431

As authors of some very large programs written entirely in 6502 Assembly Language, we've been happy to see PEEK(65) publish a series of articles on Assembly Language programming. However, we feel that such articles would not be complete without a proper review of the MnM Assembler, produced by MnM Software Tech=""

We've written several large programs which required Assembly Language for speed of execution, or special computing functions not available in BASIC. Some examples are: virtually instantaneous searches through very large data structures, extremely rapid sorting, and numeric computation to higher accuracies than that

, PEEK[65], January 1985 11

permitted in BASIC (for example, double or even triple precision arithmetic). We were able to do some of these things, slowly and awkwardly, with OSI's 65D Assembler. We desperately needed a better assembler, though, primarily for two reasons: ease of use, and an ability to assemble very large programs. We've been using the MnM Assembler for over a year now, with superb results.

But why did we want a different assembler other than the OSI-supplied one we already had? Why was the OSI 65D Assembler unsuitable for us? Let us simply list features (desirable or not) of the 65D Assembler. Then, we'll compare the MnM Assembler's features.

### OS65D Assembler:

1) Documentation: OSI published a short User's Guide years ago. (The current PEEK(65) articles are providing much of that information). Error documentation is poor, though, since Assembly errors are reported as error numbers, requiring look-up in a cross reference listing. More frustrating, though, is the occasional output of an error message with an undocumented error number!

2) Merging files: Strictly speaking, this is an operating system function, not as assembler function. Merging files is awkward under 65D. Merges require a fairly detailed knowledge of operating system calls and memory allocation.

3) Editing capabilities: Unless one purchases a separate word processor, there are very limited editing capabilities available. Only a very simple resequencer is normally available (although we wrote a much better one, if anyone is interested).

4) Symbols and labels: Assembler symbols (labels, constants, variable names, etc.) are limited to 6 characters in length.

5) Program size: All Assembly source text plus its resultant object code and the Assembler's symbol table must fit into memory at one time. Source text begins at \$317E, so a 48k computer has 36,738 bytes available to hold source code, object code, and symbol table. Assuming an 8 to 1 ratio of source code to object code (a very conservative assumption), and a 1 to 5 ratio of symbol table to source code, then assemble programs with OSI's 65D Assembler are limited to less than 4k of object code.

6) Speed of assembly: The 65D Assembler is very slow. We've seen it take more than 5 minutes to assemble progams with source text less than 4k in length.

7) Assembler problems: Assembler "lock-up" is common. Many times we had our computer simply "freeze" during assembly. We were never able to determine the cause or see any pattern to "lock-up" occurrences, other than the probability of "lock-up" was directly proportional to the length of the source text.

8) Disk storage: Since OSI's Assembler works under 65D, one does not have access to hard disk or network storage.

9) Formatting of output: OSI's 65D Assembler formats outputs consistently the same. There are no provisions for partial printouts, or forced page breaks.

### MnM Assembler:

1) Documentation: The MnM Assembler manual is a large book, with much useful information. It includes a 6502 Assembly Language tutorial section. Error documentation is in plain English.

2) Merging files: Since the MnM Assembler works under OS65U, file merges are far simpler than merges under 65D. Two files can be merged in one command line, and the procedure is always the same. 65D merges require the use of indirect files, memory pointers, etc., which will most likely be different for each merge operation.

3) Editing capabilities: By invoking the 65U editor, Assembly Language source text files can be easily loaded, edited, and saved. More extensive editing is possible with a word processor. The 65U resequencer is much more versatile than the one in 65D's Assembler. This extended resequencing power makes file merges even easier.

4) Symbols and labels: Assembler symbols can be of virtually any length. Practically, labels are limited only by the length of a single typed line. Notice how much easier source code is to understand with longer symbols.

- OSI LDY InOfst LDA InBuff,Y CMP #CR BNE ChkLF
- MnM LDY InputStringOffset LDA InputBuffer,Y CMP #CarriageReturn BNE CheckIfLineFeedWasEntered

5) Program size: The single most useful feature of the MnM Assembler is its disk-to-disk assembly. This feature perassembly. This reactive per-mits the assembly of programs whose source text is far larger than the available mem-ory of the computer. Without the MnM Assembler, really large programs such as wordprocessors, compilers, and data-base managers written in Assembly Language are nearly impossible to write for an OSI machine. In our case, many of our programs could not have been developed without the MnM Assembler. Our 6502 Simulator/ Debugger, for instance, is ap-proximately 8k in length (ob-ject code), but it is assembled from source code greater than 80k in length, more than twice the available memory under OS65D, and over three times the available memory under OS65Ul

6) Speed: MnM's Assembler runs circles around OSI's. On a 1 MHz computer, over 80k of source text (our 6502 Simulator/Debugger mentioned above) assembles in less than 5 minutes, less time than the OSI Assembler takes for files onetwentieth that size!

7) Assembler problems: None! The MnM Assembler has always worked without creating system problems, even handling disk read/write problems.

8) Disk storage: Under OS65U, one has access to all disks (floppies and hard disks), even network facilities for storage.

9) Formatting of output: MnM's Assembler is superior in output formatting to OSI's 65D Assembler. Comments are neatly lined up (all comments begin in column 40), 16-bit addresses are shown in human readable form rather than machine readable inverted lobyte/hi-byte form, the symbol table is neatly printed at the end of the assembly, and titles and subtitles are available. Subtitles force a new page form feed, so subtitled program sections stand out in easily read form. Listing commands can be inserted into the source text to permit partially printed assemblies.



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- Nested Sorts 6 Deep
- Abundant Utilities

SIMPLY POWERFUL AND COMPLETE: This DBM has external simplicity, and a manual that is written in English; complete from Tutorial to Definition of Terms. Although Gander provides support by the author, it shouldn't be necessary.

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After realizing the advantages of the MnM Assembler over the OSI 65D Assembler, we turned to MnM's exclusively. Without it, we would still be able to do 6502 Assembly Language programming, but our productivity would be so heavily impacted that we would not be able to accomplish one quarter of our present work! We heartily recommend the MnM Assembler to anyone who is seriously contemplating 6502 Assembly Language programming on OSI computers.

### PRINTER FOR OSI MODEM PROGRAM

By: Stephen B. McGinnis 505 Smith Street Ridgway, PA 15853

Sometime ago I submitted a revision of the simple Modem program originally supplied by OSI with my ClP series 2 computer. This revision automatically set the display for 48 "video characters so that the swap" routine was no longer needed. Well, the program works well (although I did I did make the modifications to make it run in 8K of memory) and I have been debating for sometime about how to get a print-ed copy of whatever happened to be coming over the tele-phone modem. With a \$10.00/ hour phone bill (weekends, no less) to access CompuServe's Pittsburgh number, my silent Heathkit H-14 printer was a constant reminder of wasted resources.

There is a simple (no, not elegant) and cheap solution to the problem: install a DPDT toggle switch. If your computer has a switch (J3) installed so that a printer, modem, or cassette recorder can be used with the serial interface, here is how to do it:

### PART NEEDED

DPDT toggle switch (similar to Radio Shack P/N 275-1546)

### TOOLS NEEDED

Soldering iron Rosin core electrical solder Solder wick (similar to Radio Shack P/N 64-2090) Screwdriver Needlenose pliers Wirestripper (or sharp knife!) OSI J3 switch diagram dated 10/21/80 (it's handy if available, but not really necessary)

### PROCEDURE

1. Unplug your computer and any attached cables, etc.

2. Turn it upside down and remove the screws holding on the bottom of the case. Lift off the bottom and expose the computer board. Position the case with the power supply located away from you and to the left.

3. Identify the following:

- a. The rotary 3-position switch mounted on the back of the computer case. This will be identified as J3. It is located immediately to the left of the opening reserved for the fan.
- b.The 25-pin Printer jack which is mounted on the back of the case. This will be identified as J5. It is the jack located next to the line cord.
- c.The 25-pin Modem jack mounted on the back of the case. This will be identified as J6. It is located between J5 and J3.

4. Identify pin 3 on the Printer jack (J5.3) and pin 2 on the Modem jack (J6.2). Notice that each is connected to J3 (the rotary switch) at the same point by brown (on mine, anyway) wires. This is J3 terminal 2 (J3.2). You have just identified the output line from the RS232 port.

5. Now, identify terminal 3 on your Modem jack (J6.3). Follow the white (?) wire to J3 and note the solder point. When J3 is switched to the Modem position, this point will be connected to the input of the RS232 port.

6. Study the DPDT switch closely. If it is from Radio Shack it should have the pins numbered from 1 to 6. If not, the 3 pins on the left of the switch are numbered 1 to 3 (top to bottom) and the 3 pins on the right are numbered from 4 to 6 (top to bottom). It doesn't matter how you hold the switch.

7. Note that at least one of the RCA jacks on the back of the case isn't being used. Later on, if you leave enough wire length (hint), you can remove one of these and install your toggle switch in its place. This will prevent you from drilling a new hole!

8. Now, de-solder the 2 wires where they attach to J3.2. Be sure you can identify these as coming from J5.3 and J6.2.

9. Solder the wire from J6.2 (Modem) to toggle switch pin

2. (Depending on where you are going to locate the switch, you may need to lengthen this wire and others later on, either by soldering and wrapping in tape or by butt connectors).

10. Solder the wire from J5.3
(Printer) to toggle switch pin
5.

ll. Cut a length of new wire
and strip 'each end. It'll
help if you apply a small
amount of solder to the bare
ends to keep the wire from
unravelling.

12. Solder one end of the wire to J3.2 (where you removed the other 2 wires).

13. Solder the other end of the wire to toggle switch pin 1.

14. Cut another length of wire and prepare the ends as before.

15. Solder one end to the point where the wire from J6.3' (Modem) connects to J3. A "tack" soldering job should suffice (note that a disk capacitor is already attached to this lug).

16. Solder the other end of the wire to toggle switch pin 6.

17. The remaining steps apply only to toggle switch connections:

- a. Using a small length of wire, jumper pin 4 to pin l.
- b. As in "a", jumper pin 3 to pin 1.



18. Except for locating the switch, you've finished.

You'll quickly find that one position of the switch will allow for the normal operation of the ClP (the switch will be pulled towards pins 3 and 6). After you've loaded your Modem program, flip the toggle switch, turn J3 to its Modem position, and all modem communications will be directed to your CRT and to your printer. Please note: the output will be at 300 baud, not the 1200 baud you've grown accustomed to at the Printer position of J3. Set your printer accordingly!

A brief diagram of the connections you made to the toggle switch follows:

RS232 out (J3.2) X (1)

to modem (J6.2) X\_(2)

RS232 out (J3.2) X (3)

- RS232 out (J3.2) X (4)
- to printer (J5.3) X\_(5)
- from modem (J6.3) and to RS232 in  $\frac{X(6)}{2}$

### $\star$

### A \$29.00 MODEM FOR OSI

By: Joseph Ennis 212 20 Street Niceville, FL 32578

THIS MODEM IS SMALL, SIMPLE, CHEAP AND WORKS! It will work equally well on any personal computer that has a serial port, preferably a 6850. The cost of the hardware (cost of kit plus transformer) is \$28.90, with another \$14.95 you can buy the software. There are lots of reasons to add a modem to your OSI. TO me the top three reasons are: 1) my own personal terminal for the times when I have to compete for the use of a terminal; 2) to swap software with non OSI personal com-puters; and 3) with the availability of computer nets, it might make more sense to buy an access number than a disk drive for bulk storage.

### HARDWARE DESCRIPTION

The modem is a kit produced by

Electronic Systems, Dept KB, P. O. Box 18220, San Jose, CA 95158, and has been around since at least 1977. The kit contains: 3 IC and about two dozen discretes on a double sided 2 1/2 inch by 2/1/2 inch glass board. The IC are sock-eted and the kit takes less than 1/2 hour to build. SUPER SIMPLE: The inputs to the modem can be either TTL or RS-232 from the computer side and Bell A-103 at any audio level from 3 mv. to 3 volts on the telephone side. This modem is configured as an originateonly type and although it can easily be set for answer-only tones, a single model cannot easily be switched back and forth between originate and answer. If one needs both functions, it would be ad-visable to buy two of these modems and configure one each way and switch them as needed. Also, there are no duplex nor baud rate controls on this modem.

These are handled by the software in conjunction with the ACIA (Asynchronous Communications Interface Adapter), the 6850 on the OSI board. The modem is designed to drive an audio coupler and the kit instructions tell how to connect one. To keep down size and cost, I chose direct connection. For those that want the acoustic coupler option, Rondure/Computer, Room 2522, Butler, Dallas, TEXAS 75235, sells a low cost one for \$19.95. So far, the cheapest direct coupler that I have found is a 600 ohm to 8 ohm subminiature transformer from Radio Shack. Since the tele-phones in my area are touch tone with modular plugs, my direct coupler has an ex-tension cable with a modular plug on the end. To keep the telephone company happy, the connection to the telephone line should be around 600 ohms inductive reactance, 200 ohm resistive and deal with audio levels between 3 mv. and 1.7 volt. The Radio Shack transformer has 77 ohm primary DC resistance so a 120 ohm 1/4 watt resistor was placed in series with the primary. The audio levels are around one volt on the telephone line so they do not need any changes. (Figure 2 shows the schematic for the two boards.)

The modem can be connected to either the OSI RS232 port or the TTL port. Since I want to keep the RS-232 port open for a printer and since the TTL port would never be used for anything, I chose to drive the modem from the TTL port. If

your OSI does not have com-ponents in either of these blocks of circuitry (the usual case), then you will have to use Photograph 2 and 3 and the schematic in Figure 1 for parts, values and placement to populate these circuits. Unfortunately, a modification will have to be made to the circuit track on the 600 board to use this modem, for the TTL port, or even the RS-232 port. It is not a big mod, just cut the jumper at W-12 (See Figure 1 for what W-12 does.) W-12 is necessary because the cassette port is anti-social. There are three ports that the ACIA, U14 could connect to; the cassette port, the RS-232 port, and the TTL port. The transmit part of the ACIA connects to all three with no problems. The problem is with OSI's implementation of Kansas City Standard for the the cassette port. When there is no signal coming in on the cassette port, the output of the Kansas City Standard circuit (U63), cassette RxData is held low. A little better design would have resulted in the cassette RxData line being in the high-don't-care state. Then all three RxData lines could be tied together in an implied-OR circuit. Therefore, the easiest way to deal with this is to switch the cassette RxData line out when it is not needed. (Make the cut at W-12 as shown in Photograph 2.)

I originally added a jumper from W-12, pad C to J3-8 (an unused pin) and switched J3-4between J3-5 and J3-8. (See schematic in Figure 1.) This proved to be an unnecessary jumper, the circuit works just



as well if a much shorter jumper is placed between W-12 pads A and C, and only a single pole, single throw switch is used to jumper J3-4 and 5 when one wishes to load a cassette tape into the computer. Two additions that I added to the interface board are: 1) a LED and a resistor to act as a carrier detect indicator, and 2) a jack to be used by a touch tone dialer (See Figure 2).

### SOFTWARE

Most Software Houses that write programs for OSI computers have some version of "Dumb Terminal" or a modem driver program. I have bought both the Progressive Computing and Aardvark Technical Services, (2352 South Commerce, Walled prefer the Aardvark, it is a little better document little better documented. For purposes of checking this installation, I offer a Simple (like in low IQ) terminal pro-gram, given in List 1. This This program will allow you to establish two-way communications.

HOW IT WORKS - HARDWARE

As long as one is the orig-inator of the communication, the hardware requirements for a modem is very simple. The answering modem has to do all the work of checking and matching baud rate and word format. Therefore, if one eliminates the circuits that are used to do this checking and switch setting, one finds that all that is left is one voltage controlled oscillator (a single IC) and a single IC). That is what this modem card is: two phase-locked loops, one CMOS hex inverter, a switching transistor, two frequency adjustment pots, 5 timing quality capacitors and not much else. It runs off a single +5 volt power supply. Referring to Figure 2, Ul (the 2211) is a straight forward tone discriminator, pin 7 is either high or low depending on whether the audio tone present at pin 2 is above or below the time constant formed by R5, R6 and C6. The audio received from the telephone line comes through the decoup-ling transformer Tl to Ul pin 2, the tones are either 2225 Hz (logic one, also called a Mark, the carrier rests at Mark, the carrier rests at this frequency when no data is being sent), or 2025 Hz (logic zero, also called a Space). The whole job of Ul is to toggle its pin 7 between high and low as the signal on its

40 REM List #1, Simple Terminal. 50 POKE235, 32: POKE236, 239: POKE237, 255: POKE238, 96: POKE530, 1 60 POKE11,232:POKE12,0:POKE232,32:POKE233,235:POKE234,255 70 PRINT"SIMPLE TERMINAL":PRINT S0 INPUT\*FULL OR HALF DUPLEX\*;A\$:IFLEFT\$(A\$,1)=\*F\*THENF=128 90 PRINT: PRINT 300 BAUD IS NORMAL, TO GET 75 BAUD ADD 1 TO CONT"; 100 PRINT\*ROL WORDS. TO GET\*: PRINT\*4800 BAUD SUBTRACT 1.\* - 110 PRINT: PRINTTAB(2)\*300 BAUD CONTROL WORDS\*: PRINT 120 PRINT\*WORD NO CHRS PARITY STOP\* 7 130 PRINT" 01 EVEN 2" 140 PRINT" 05 7 000 2 \* 1 " 150 PRINT\* 09 EVEN 7 160 PRINT\* 13 7 000 1 " 170 PRINT" 17 8 EVEN 2. 180 PRINT\* 21 1 \* 8 ÚDD 190 PRINT" 25 3 EVEN 1 " 200 PRINT\* 29 1 \* 8 000 218 PRINT: INPUT\*ENTER ACIA CONTROL WORD\*; BAUD -220 STATUS=61440:BUFFER=61441:KEYBOARD=57088:NOOP=254:RESET=3 230 PARITYERR=64:FRAMERR=16 240 SAVE:POKESTATUS, RESET: POKESTATUS, BAUD: PRINTCHR\$(26) 250 P=PEEK(KEY): IFP<>NOOPTHENPOKE108, F:X=USR(X): POKE108, 0 260 P=PEEK(STATUS): IFNOT(PAND1)THEN250 280 IFPANDFRAMETHENX=PEEK(BUFFER):GOT0250 290 IFPANDPARITYTHENX=PEEK(BUFFER):GOT0250 300 X-PEEK(BUFFER); IFX)127THENX=X-128 310 POKE517, 0: PRINTCHR\$(X); : POKE517, 1: GOT0250 ready

### LISTING 2

### SIMPLE TERMINAL

FULL OR HALF DUPLEX? FULL

300 BAUD IS NORMAL, TO GET 75 BAUD ADD 1 TO CONTROL WORDS.

TO GET 4800 BAUD SUBTRACT 1.

300 BAUD CONTROL WORDS

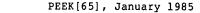
d Q D	NO	CUDC	PARITY	CODD	
	110	CHRS	PARTII	STOP	
Ø1		7	EV EN	2	
Ø5		7	ODD	2	•
Ø9		7	EVEN	1	
13		7	ODD	1	
17		8	<b>EV EN</b>	2	
21		8	ODD	1	
25		8	EVEN	1	
29		8	ODD	1	

#### ENTER ACIA CONTROL WORD? 17

input pin switches between 2225 and 2025 Hz. Since the output at pin 7 is low for a frequency higher than the RC time constant, it must be logically inverted before it is sent to the ACIA. This is achieved by one of the inverters in U3 (pin 12). This output connects to the 600 board (J2 pin 1) and becomes RXData3 going to W-12, and then on to the ACIA (600 board U14 pin 2). R4 on the modem card sets the band width or depth of null for the discriminator. In summary, U1 and U3 on the modem card are a frequency to TTL logic level converter and the rest of the receive function is done either by the ACIA or the software. The modem transmit side is even simpler, U2 (a 567) is also intended for tone demodulator service, and it has a very accessible VCO (Voltage Controlled Oscillator), and is all that is used in the 567. The timing for the 567 is con-trolled by the resistances and capactances connected to pins 5 and 6 (see Figure 2). If Q1, the 2N222, switching transistor is biased off, then only C9 and C12 in conjunction with R8 and R9 set the frequency of the oscillator. When Ql is biased on, C7 and C8 are added to the RC circuit causing the oscillator to oscil-late at a lower frequency. For the modem transmit side, the frequencies are 1270 Hz (logic one, Mark, and is where the carrier rests when there is no data to transmit) and 1070 Hz (logic zero, Space). Again the logic is inverted, so one of the inverters is connected to the input of Q1. The rest of the inverters are used as audio drivers on the output audio to the telephone line. These output drivers are the only reason that the inverter has to be CMOS, anything else would load the oscillator pick-off point.

HOW IT WORKS -- SOFTWARE

If you already have a terminal /modem program, just LOAD and RUN and skip to the alignment section. If you just built this modem, and don't have any support software, but just can't wait to try it out, a modem demo program is given in List 1.

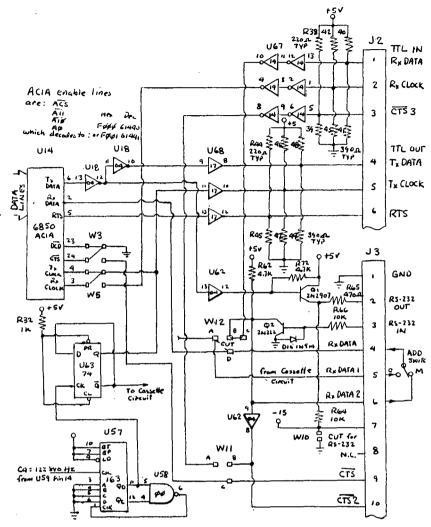


Before discussing how the software works, a review at the system level is in order. An Originate modem can only talk to an Answer modem and an Answer modem to an Originate modem. If both of you have Originate-only modem, no pos-sible software mod will allow you to communicate; however, you could talk to a third party such as a computer bulletin board, which is always an Answer modem. With the bulletin board one could post a message or program for the other to copy. For the case where you will only be using your modem to talk to one other modem connected to an OSI computer (or most other personal compu-ters using BASIC), no software is needed at all. One modem will still have to be tuned to the Answer frequencies. But for communicating and swapping programs, the SAVE and LOADS -which you already have in your computer's system monitor ROMS - is all the software that is required. One calls the other on the telephone, and both computers and their modems are energized and connected. Then Then when one person is ready to send something, he tells the other person to type LOAD CR on her end, then he types SAVE CR and LIST CR on his end, The data that's all. qoes over the telephone line just as if it was going to or coming from the cassette port. Voice communication can occur at the same time. You ignore the tones the computers are putting on the telephone line and the computers ignore the voices.

the ACIA (Asynchronous Now, Communication Interface Adapter) needs to be discussed. In the OSI Computer, the ACIA is a 6850 chip. The ACIA has four internal 8 bit registers; however, two are write-only and two are read-only on the CPU side of the ACIA, so the only four registers take up two address spaces. The receive buffer and transmit buffer are one pair and the control register and status register are the other pair. When the ACIA is initialized, a control word is written to the control register by the System Monitor ROM, that selects one of several programs built into the ACIA and sets the divide-by value on the baud rate clock divider (sets baud rate). The ACIA uses its built-in program to deal with interrupts, to take in an 8 bit word from the CPU and shift it out, a bit at a time, on each (baud rate) clock pulse, receive a string of pulses and assemble them into an 8 bit word, and sets flags

### FIGURE 1

Partial Schematic of OSI 600 Board Showing ACIA, RS-232 Port, and TTL Port



in the status register so the CPU can check on the ACIA's progress.

Now, finally, how the software in List 1 works. The program first presents a menu, and you select the communication mode you wish to use. This results in the ACIA's Control Register being reset and the desired control word being entered. Line 240 does the work. Now the program loops continuously between lines 250 and 270. Line 250 PEEK's the keyboard for a key closure and if there is one, it uses a USR (X) routine to call part of the ROM's monitor routine (the vector was set in Line 60) which will do all the work of outputting one ASCII character (equivalent to the key press-ed) to the ACIA's transmit buffer address.

Now control passes to Line 260 which PEEK's the ACIA's status

buffer and checks if the receive buffer flag is set. If it is, it jumps to the subroutine on Line 280, and if not, it loops back to Line 250. Subroutine at Line 280 checks the status word for indications of errors and if none, converts the contents of the receive buffer into an 8 bit ASCII word by chopping off the parity and stop bits, Line 300 does the work. Line 310 prints the resulting character to the screen, then returns control to the calling line.

The received communication goes only to the screen in normal mode, and does not trigger the BASIC interpreter. If you want to send the received communication to memory by way of the BASIC interpreter, first make sure there are a lot of NULL's after each line and there are not any line numbers in the program to be received that correspond to

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the line numbers in the Modem Program, then hit the BREAK key, and W key. At this point you are in Local Mode. Now what you type on the keyboard goes only to your computer, and you can type anything anything except something that looks like a line number or something that returns an error message; however, the Modem is still sending the MARK tone so the line to the other computer is still being kept up. so, type LOAD (and of course, a carriage return). (If using HEXDOS type POKE 517,1 in-stead). Now until you hit either the Space Bar or BREAK Key all in-coming communication will go through the BASIC language interpreter in your computer, which will pack it in memory, if possible. The results will also scroll on the CRT screen. When the communication is complete and is not to contain observed anything that would be detrimental, hit the BREAK Key and the W Key again. Now you either RUN the program can by typing RUN 320 (and return) to see what you have. Or could go back to two-way Or you communication by typing RUN 240 THAT'S (and return). ALL THERE IS, TO HOW IT WORKS AND HOW TO USE IT.

### CHECKOUT AND ALIGNMENT

If you have a frequency counter, an audio oscillator and an oscilloscope, you are in luck! The kit instructions give the alignment procedure and it takes less than five minutes (only two pots to set). Do it and skip the rest of this article. Otherwise, we will have to do it the hard way. instructions The Modem kit give three different techniques for those without test equipment. I will add a fourth technique for those that don't have test equipment but are computer timeshare users:

1) Borrow a terminal.

 Using the terminal, signon the timeshare normally.

3) With the carrier up but no characters being sent either way, and Modem Pin 5 disconnected from the transformer, adjust R6, the pot closest to Ul (XR2211) for a High Level (or if the carrier detect LED option is connected, the LED ON) at the Modem Pin 4.

4) Load and start "Simple Terminal" or any other Modem program.

5) Go back to the borrowed terminal and command the Host

FIGURE 2

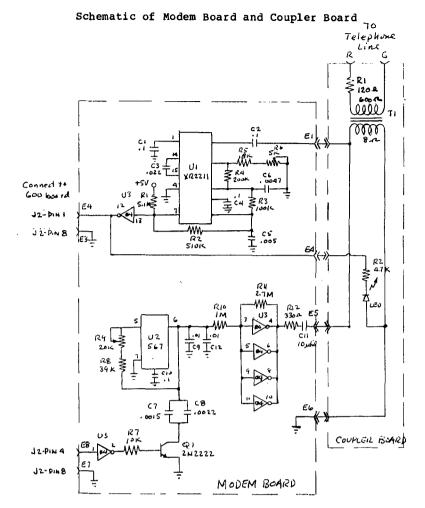
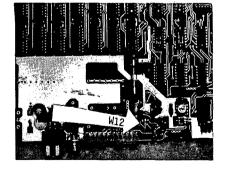


Photo #1

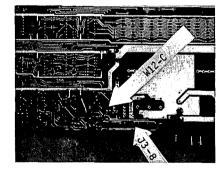




Direct Coupler Front/Back View



Modem Front & Back View



### Photo #2

OSI Board Top View Showing Track Cut at W12. Photo #3

OSI Board Bottom View showing Jumper from W12-C to J3-8.

Computer to list a long file. R6 is already close to its proper adjustment point, and this file coming from the Host is the test pattern that will allow R6 to be fine tuned to its final position.

6) Go back to R6. A very small clockwise adjustment should result in the characters that are being transmitted by the Host to start being printed intelligently on your Computer's CRT. DOESN'T THAT LOOK GOOD! When R6 is close, characters will be printed but they will not be the right character and there will be a lot of skipped characters. When R6 is adjusted correctly, characters. all the characters will be correct; however, there may still be some skipping as the Baud Rate Clock in the terminal is not necessarily the same frequency as the Baud Rate Clock in your computer This (most likely won't be). is because, when the Host is slaved to the borrowed terminal, your computer will be drifting through sync, this will cause occasional characters to be missed.

7) Now connect Modem Pin 5 to Modem Pin 1 permanently. This puts the Modem transmit carrier on the telephone line. Restart the Host, listing out that long file. Adjust R9 in small steps until the Modem transmitter carrier steals sync from the borrowed terminal, this will be indicated by the string of characters being printed to the CRT screen being printed without misses.

8) Now disconnect the borrowed terminal, and observe that the Host still stays on the line (if the LED option was added, the LED should stay lit) and recognizes your Modem as a Modem. Now, try pressing a few keys on your computer's keyboard, if the Host responds then the alignment is done.

9) If not, mark where R9 screwdriver slot is located and adjust R9 back and forth about one screwdriver slot width, hunting for the spot of best operation. When the alignment is complete, place a drop of fingernail polish on R9 and R6 and close the case.

Good luck and enjoy!

### LETTERS

### ED:

In my letter of July 30th concerning the WP6502 Word Processor and a letter from Carl King in the June issue, I stated that I was working on disassembling the WP6502. This was started in order to make the following modifications:

While printing; to be able to stop it and return to the Menu.

Replace the R/Tape and W/Tape in the Menu with Print selections.

Go with View and one tap of the space bar to the screen display.

The disassembling has progressed far enough so that the modifications could be made. Since my system does not use a cassette, the space for the R/Tape and W/Tape coding is used for the new coding. An escape routine was added so that the "ESC" key, with the Shift Lock up or down, will stop the printing or screen viewing and return to the Menu.

The R/Tape in the Menu is changed to P/Std and coding is added that sets up the parameters for printing at the default values. The values are: Device 01, Margin 10, Form length 66, Text width 60, Page No. 00, Number of copies 01, AP Yes, Ho No. A "P" reply to the Menu, will start printing.

The W/Tape in the Menu is changed to OptPrt and the coding is changed so that an "O" reply will display the printer Menu so that the parameters may be changed by entry before the printing starts, just as originally written.

Does anyone know of a new address for Dwo Quong Fok Lok Sow or if they are still in business?

J. Edward Loeffler, Jr. Huntsville, TX 77340

Edward:

We **ål**so have tried communicating with Dwo Quong, and like you have been unsuccessful. We fear the news it not good.

Peek Staff

\* \* \* \* \*

ED :

I would like to point out a problem with the device driver for device #1 in OS-65D v3.2 and v3.3. On my ClP mod 1, with a printer on the output side of device #1 and nothing

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TAXTIME FUN???

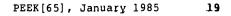
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on the input side, the system will hang or issue a break while printing to this device. Referring to the dump in figure 1 shows the reason. After successfully transmitting a character OSI has decided to implement the <ctrl><s>,<ctrl>
<q> protocol. However, input
for this is taken, not from
the current input device according to the I/O distributor, but from device #1. Since there is nothing connected to the input side of device #1, it is reading garside of bage! A random <ctrl><s> will hang the system and a <ctrl> <c> will cause a break to be issued. The problem can be corrected by including a POKE9433,96 at the beginning of BEXEC\*. This will cause the device driver routine to return immediately after а character has been successfully transmitted.

Figure 1 Output Device Driver #1 Replace \$24D9 PHA with RTS

> 24CD 48 PHA 24CE ADOOFO LDA \$FOOO 24D1 4A LSR A 24D2 4A LSR A 24D3 90F9 BCC \$24CE 2405 68 PLA 2406 8001F0 STA \$F001 24D9 48 PHΔ 24DA ADOOFO LDA \$F000 24DD 4A LSR A BCC \$24F1 24DE 9011 24E0 20F624 JSR \$24F6 24E3 8D2523 STA \$2325 24E6 C913 CMP #\$13 BNE \$24F1 24E8 D007 24EA 20F624 JSR \$24F6 24ED\_C911 CMP #\$11 24EF DOF9 BNE \$24EA 24F1 68 PLA 24F2 8D6323 STA \$2363 24F5 60 RTS 24F6 AD00F0 LDA \$F000 24F9 EE2423 INC \$2324 24EC 4A LSR A 24FD 90F7 BCC \$24F6 24FF AD01F0 LDA \$F001 2502 297F AND #\$7F 2504 8D6323 STA \$2363 2507 60 RTS

Frank Glandorf Cincinnati, OH 45220

\* \* \* \* \*

ED:

You have at least one devoted follower of Rick Trethewey's Assembly Programming class, but I would like to offer a couple of comments.

Your sample programs are impossible on a serial system could you please include a couple of beginners samples for those of us who don't own video based systems? I realize that I am jumping a number of lessons ahead but I hope you include some information on interfacing to a BASIC program. As an example, I have input a string under BASIC and now want to manipulate the string in machine language. I know the machine program can be loaded at \$6000 in front of BASIC, but how do I call the required string into the work area and return it (probably a different length) to the BASIC area?

Another that comes to mind are printer and VDT commands. These are currently held in a dimensioned string and must be loaded from disk and created under CHR\$ for each program run. It is a 22 dimension and up to 5 characters in each string - thus time consuming. Obviously, a machine routine at \$6000 would speed this up tremendously, but how do I put the dimensioned strings where BASIC can use them?

The other area I hope you will include is input - output to a serial VDT.

Now to give you a laugh - I have almost completed my string manipulation routine. It is currently 161 bytes and has taken only three days of programming mistakes, hang ups, and "Hell, why did it do that?!"

It is my first attempt, and no doubt looks it, but hopefully practice will improve speed and method.

Thanks for the lessons, keep them coming.

Ian Mutch Queensland, Australia

Ian:

Rick Trethewey reports that he spent most of a day working on the string interface to BASIC before success. That done, he has written lesson #9 of the Assembly Language tutorial.

Hang in there, it's coming.

Peek Staff

\* \* \* \* \*

ED:

Month after month I see your request for articles by business people about their computer applications at work. I'd like to suggest a possible reason why you haven't received many such articles in the past - SECURITY.

It sounds a little like para-

noia, but in our cut-throat economy, many business people are reluctant to reveal anything about their operations lest a competitor should profit from such a revelation. This would obviously apply where a firm in a given industry has developed some possiunique software. blv The developer would naturally feel that his/her software is what makes him/her more successful than those without it. To have an article published about that software (or its application) could reduce his/her company's edge over competitors in the market.

As a counter-argument, I suggest that such reluctant business people submit articles merely describing the hardware. The software could be described with such generic terms as "payroll, inventory, general ledger", etc. No details of the origin of the software or its special features need be given.

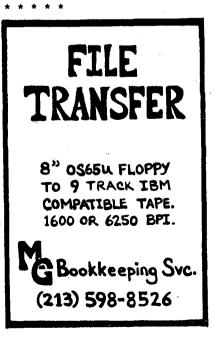
So, c'mon you lawyers, doctors & accountants! Tell us that you're using an OSI computer not some other brand. And please tell us how much time it has saved you and how much more productive you are because of it. Tell us how you came to choose OSI instead of BRAND X. After all, we want to hear about the computer, not your trade secrets.

Bruce Showalter Abilene, TX 79601

Bruce:

Hear! Hear!

Eddie



I recently purchased all of the available back issues of PEEK(65) and read through them. I recommend this -- a fascinating narrative on OSI!

I called Hazeltine at their (516) 549-8800 number recently and got Police Emergency three in Greenlawn, NY-- no forwarding number. Tried Information and was given (516) 261-7000 Hazeltine. They gave me 293-5600, Technical Support. Tech Support said that Hazeltine doesn't support the 1400/1500 line any more -- try TRW in Jersey, (201) 575-7110. I did, and eventually reached the Tech Rep, Jack Riggs at ext. 5289. He's a real help cooperative and knowledgeable. They sell parts, too.

Does anyone out there have a 9-track tape drive, a hard disk, and possibly FIBACK? I have 4 or 5 files on tape (500K bytes or so each) and need them on OS-65U floppy so I can use them. \$, trade, etc..

Are OSI'S OS-65U utility routines copyrighted? I'm writing enhanced versions of CREATE, DELETE, and possibly PACKER and COMKIL, which I might want to sell for a small Whatever happened to the promised annotated OS-65U disassemblies? Since PEEK(65) told me that they don't know of any on the market, I'd like to do one -- I need it. If anyone out there has partials that they'd like to share, I'll provide a copy of the finished product in exchange for a significant (i.e., several routines) annotated disassembly (1.43 or 1.44).

Has anyone designed a mod or a kit to a 470 board to read IBM 3740 format? (J.B. Boardman's letter in the Oct '81 PEEK(65) is a start).

Does anyone have an OS-65U based routine which will read either OSI CPM or IBM 3740 format floppies?

What's happened to Rockwell's R65C02 pin-compatible 6502 chip with an enhanced instruction set and up to a 4 MHz clock? (I saw it mentioned in PEEK(65) about 18 months ago.)

Steve Hendrix in the October '83 PEEK(65) discussed the ClP's CPU board trying to drive the memory boards at 2 MHz and not always being successful. I've had the same sort of problem with a 510 and 3 520s -- is it the same problem with a similar fix?

Tom McGourin 1170 Nestor Ave., Apt. Al6 Akron, OH 44314 (216) 745-3516

Tom:

Let's see. One thing at a time!

1) 9 track to floppy service is available at a number of places, two come to mind: MG Bookkeeping Service advertised in November, page 21, and although not advertised, Mike Sokol, also in November, page 19, is equipped to do it too!

2) Copyrighting is always ticklish. Yes, OSI does copyright. The question is, how much modifying have you done? It is a legal point. Best consult OSI. In the past they have been cooperative. It is to their advantage to have their machines work better.

3) We have a disassembly of OS-U in hand, just awaiting go ahead instructions from the authors. We would be happy to cooperate with you in anyway to get this information to those who need it.

4) If there is something new

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on 3740 readability, our readers haven't told us yet. The closest we can come is D&N's Disk Transfer.

5) R65C02: Shy of substitution for the sake of substitution, there remain a number of problems to utilize its power. For starters we need 4 MHz RAM. See page 10, Thorsen.

6) Let's ask the master. What do you say, Steve?

### Eddie

\* \* \* \* \*

Tom:

The problem with the ClP trying to drive memory boards at 2 MHz stemmed from a shortcut by OSI, in that they did not include any buffering in the phase-2 clock signal, but made the processor output pin try to drive the whole system. Even with the power-hungry 2114s, it was up to the job as long as the clock stayed at 1 MHz. I got a real shock the first time I hooked an oscilloscope on the bus with it heavily loaded, and observed the "square wave" as something more like a logarithmic ramp. Since memories or peripherals require a certain voltage before they consider the signal to be at a "true" level, a sharper rise time results in the signal effectively coming on sooner, which gives the device more time to respond before the processor latches the data. The effect is not linear: if the signal takes 250 nsec to rise to a "true", it leaves an additional 250 nsec for the memory to respond at 1 MHz (500 nsec low, 500 nsec high), but no time at all at 2 MHz (250 nsec low, 250 nsec high). If the rise time is longer or the clock is faster, the result is that the memory must respond sometime before it gets the signal, and I don't think there are any chips around yet that are are quite that fast.

Though I am not familiar with the 510 and 520 boards specifically, it sounds like there would be more than enough devices driven by phase-2 to cause a similar problem. Even with a TTL buffer on my system, the problem does not go away completely. The processor phase-2 output is rated to drive one TTL load, but even standard "LS" series TTL is only rated to drive 10 additional inputs. A conservative design would require a separate driver for each row of memory chips, such that There would only be a total of ten inputs being driven from the bus. Of course, each additional level of buffering adds its own delays. In addition, the fact that there are several different boards being driven by a common bus with some significant length calls transmission-line for some theory (and I know just enough of that to be dangerous). In any case, the processor board should probably drive the system phase-2 signal through buffer with some muscle, perhaps an 8T26 or 8T28 (the 8T26 inverts the signal; 8T28 does not) The cle the 8T28 does not). The cleaner rise on the clock signal with a better driver results in more time for the memories to respond.

Steve Hendrix 4089 Alexander Rd. Atwater, OH 44201

\* \* \* \* \*

3 REM

to the satellites in synchronous orbit. As you may know, there has been a great increase in the popularity of the home receiver or "Earth Station" recently. This program will help in aiming the dish, providing the azimuth and elevation angles necessary to point the thing in the right direction.

As you get into the program, you will notice that the coordinates of the earth station are in degrees. I find that it is not necessary to get to minutes and seconds, because measurements that precise are not always known, and because the dish will be peaked on the satellite by observing the picture. In line 35, the user should insert his own city, and his own coordinates in line 50. This helps speed the data entry when running the

4 REM PROGRAM NAME IS 'SAT' REM WRITTEN BY DWIGHT FINGER, JULY, 1984 REM 7 REM 9 GOSUB 5000 10 PRINT TAB(16)"THIS IS A PROGRÁM TO CALCULATE LOOK ANGLES TO " 20 PRINT TAB(24)" SATELLITES IN SYNCHRONOUS ORBIT" 25. FOR ImiTO7:PRINT:NEXT I 30 DO=0:K=6.626:C=.0174533 35 PRINT TAB(16)\*15 EARTH STATION LOCATED IN ANCHORAGE? Y or N"; 40 INPUT AS 50 IF LEFT\$(A\$,1)="Y" THEN L1=61.19:L2=149.81:60TO 200 53 IF A\$(>"N" THEN 30 53 IF A\$()\*N\* THEN 30 55 PRINT 60 PRINT TAB(13)\*ENTER THE LATITUDE AND LONGITUDE OF THE EARTH STATION\* 65 PRINT TAB(17)\*IN DEGREES, SEPARATED BY A COMMA\*; 70 PRINT;INPUT L1,L2 80 IF L1>90 DR L2>180 THEN 60 200 PRINT;PRINT TAB(21)\*ENTER THE LONGITUDE OF THE SATELLITE\*; 202 INPUT SA 203 LA=L1\*C:L0=L2\*C:S=SA\*C 218 D=S-L0 210 D=S-LU 215 IF LA=0 AND D=0 THEN DO=1 : REM CHECK FOR DIRECTLY OVERHEAD 217 IF LA=0 AND D=0 THEN DO\$="THE SATELLITE IS DIRECTLY OVERHEAD" 219 IF DO=1 THEN GOSUB 50001PRINT DO\$:GOTO300 220 IF LA=0 AND S\LO THEN A2=290.00:GOTO270 222 IF LA=0 AND LO\S THEN A2=90.00:GOTO270 228 REM 230 REM CALCULATE AZ 240 REM 250 AZ=ATN((TAN(D))/(SIN(LA))) 260 AZ=(A2\*37.29)\*180 265 AZ=A2\*100:AZ=INT(A2):AZ=AZ/100 : REM ROUNDS TO 2 DEC PL 278 GOSUB 5000:PRINT:PRINT"THE AZIMUTH IS";AZ;"DEGREES" 300 REM 310 REM CALCULATE ELEVATION 320 REM 325 M=COS(LA):N=COS(D) 338 X=((K\*M\*N)-1)/(SOR(((K\*K)+1)-((2\*K)\*M\*N))) 348 EL=ATN(X/SOR(-X\*X+1)) 340 EL=ATN(X/SQR(-X\*X\*1)) 345 EL=EL\*5729:EL=INT(EL):EL=EL/100 : REM ROUNDS TO 2 DEC PL 350 PRINT:PRINT"THE ELEVATION IS";EL;"DEGREES" 360 PRINT:INPUT"HARD COPY? (Y)es or (CR)=No";GA\$ 370 IF LEFT\*(GA, I)="Y" THEN GOSUB 2000 500 PRINT:INPUT"DO YOU WANT TO TRY ANOTHER? Y or N";GA 520 IF LEFT\*(GA, I)="Y" THEN GOSUB 5000:CLEAR:GOTO 30 530 IF LEFT\*(GA, I)="Y" THEN PRINT"END OF PROGRAM" 530 RUN"BEXEC\*","PASS" 1000 REM 2010 REM \*\*\*\*\* SUBROUTINES 2020 REM 2020 REM 2020 REM 2030 PRINT#5,"FOR A SATELLITE LOCATED AT";SA;"DEGREES," 2035 PRINT#5,"FROM AN EARTH STATION AT";L1;",";L2 2036 IF DO=1 THEN PRINT#5,D00:GOTO 2060 2040 PRINT#5,"THE ELEVATION ANGLE IS";EL;"DEGREES AND " 2050 PRINT#5;PRINT#5;PRINT#5;RETURN : REM TO LINE 500 5000 REM 5018 REM 5018 REM 5030 PRINT CHR\$(126);CHR\$(28) ; RETURN ; REM CLS AND HOME

### ED:

As promised, here is the program to calculate look-angles gram allows input of other

program if the user is inter- '



coordinates for other locations.

The progam is written with North America in mind, and the entries of Long. and Lat. are positive numbers, degrees west and degrees north. The satellites are also positive numbers, in degrees west. To be honest, I don't know if the program will work under any other conditions.

The rest of the program should be self-explanatory by means of the REMs. As previously mentioned, I'm a bit new at this and there may be better ways of doing some operations, but this is what I have come up with and it does seem to work.

Dwight Finger Anchorage, AK 99504

### ERRATA

### ED:

After years of cursing faulty program listings, I am ashamed to admit to creating one of my own. Please publish these corrections to the PIACOM program in the December issue:

Line 545: the variable ΒY should be BR

Line 585 should read:

EB=BU+BY:TN=INT(RT\*BR/BY): FZ = INT((RT+1)\*BY-TN\*BR)

BY is the number of bytes per track in the buffer. BR is the number of bytes per record in a random file.

While I'm at it, I also noticed that I twice used the ex-



pression "a non-ASCII character" in the documentation when what I meant, of course, is a non-alphanumeric character--a graphics or control character that would not occur in a text file.

My apologies for any confusion this may have caused.

Steve Donachie Miami, FL 33143

### WAZZAT CORNER!

### Continued from page 4.

REM PRINT USING BY LZ JANKOWSKI

: FRINT 1(28):INFUT "How many numbers ";H:DIM NS(H):FRINT FOR 0-1 TO H : INFUT "Number "; N: GOSUE 190 NEXT EXD ī0 30 40 50 60

70 1 100 25(1)=\*00\*; 15(2)=\*0\*; T=40 200 M\$=STR5(INT((M.005)+100));S5=LEFT5(M5,1):L=LEN(M5)=1 210 M\$=RIGHT(M5,L): IF LC3 THEN M5=25(L)=H5: L=3 230 FINT THE(T=L=2) S5 LEFT5(M5,L=2) \*.\* RIGHT5(M5,2) 240 LEFRELD; RETURN

REM PRINT USING, VERSION 2 - BY LL JANKOWSKI

5 FOR 0-1 TO 9: 15(0-15(C-1)\*\*0': NEXT: P=40 6 FRIDT: [28]: IRFUT"How many decimal places \*, D: 1=10'D 10 FRIDT: INFUT "How many numbers \*, H: DIM NS(H): FRINT 30 FOR 0-1 TO H 40 : DEFUT "NAMBER \*, N 50 : IF N=000 000 000/I THEN FRINT\*\* too large!\*;GOTO 40 60 : GOOMB 200 70 NEXT

90 : 200 MS-STRŠ(INT((N-5/1)\*1)): SS-LEPTS(M5,1):L+LEN(M5)-1 210 MS-RUCHTS(M5,L): IF LCD-1 THE: MS-25(D)+MS: L=D+1 230 PRINT TAB(P-L-2) SS LEPTS(MS,L-D) \*.\* RIGHTS(MS,D) 240 L=PFRE(L):RETURN

How useful is the program for OS65U users?

ADS

Good news to the owners of ClP-MF, C2P-MF or C4P-MF OSI Computers with Single Disk Drives, 'Fantastic Copy II' has just been released by the author, Mike Putnam, with the copy guard removed allowing you to copy the copier ... it also works with drives that have head unload and motor shut-off modifications best of all, the price is only \$15.00 for all versions and we will even update your present Order today disk for \$7.50. from Thomas Technical Service, RD #1, Box 135, Linden, PA 17744. Mastercharge, Visa and COD's, phone 717-398-1893. Also ask for our bargain filled catalog of OSI used computers, boards and software.

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\* \* \* \* \*

Send for free catalog, Aurora Software, 37 South Mitchell, Arlington Heights, IL 60005. Arlington Heights, Phone (312) 259-4071.

\* \* \* \* \*

FOR SALE: C4P, cassette based, 8K. Has OSI manuals, Sams Photo Facts Manual, about 1 dozen cassette programs (some Aardvark) a few years accumu-lation of PEEK(65)s and Aardvark journals. \$75 (negoti-able). Steven Defoster, 23 Cleveland Ave., Binghamton, NY 13905, (607) 797-4625 after 6 on weekdays. Anytime on weekends.

\* \* \* \* \*

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\* \* \* \* \*

OSI C3A for sale. 56K RAM, 2 DISKS, OSI Software plus, CPM Wordmaster and Spellguard, plus Epson MH-80 printer. plus Epson MH-80 printer. \$650, C3A alone: \$400. Brian Brian Armstrong, 671 Live Oak, Menlo Park, CA 94025, (415) 325-7886.

\* \* \* \* \*

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