

3400

**Control Data® 3400 Computer System
General Information Manual**

Any comments concerning this manual should be addressed to:

CONTROL DATA CORPORATION
Documentation Department
3145 PORTER DRIVE
PALO ALTO, CALIFORNIA

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INTRODUCTION

The Control Data[®] 3400 computer system, a complex data processing system with maximum computing power, combines hardware of advanced design with a complete set of automatic programming systems. It operates under program control with minimum operator intervention.

Primarily scientific in orientation, the 3400 system can be easily adapted to business data processing needs. Engineering, administration, manufacturing and marketing can all be handled efficiently by one 3400 computer system.

Using a 160A computer with the 3400 will result in an off-line system capable of handling great quantities of data. The 160A computer can perform all off-line functions in the system as well as many routine conversion and computation operations. Simultaneously, the 3400 computer can deal exclusively with high speed tape units, insuring maximum speed and efficiency for the over-all system.

CONTROL DATA 3400 COMPUTER SYSTEM

The CONTROL DATA 3400 Computer System has a storage capacity of 32,768 words and rapid data transmission. It is compatible with the 3600 Computer System. Available options permit a wide variety of configurations. Peripheral equipment includes the following:

- Single or dual channel capability on high speed printers, card readers, card punches, and disc files
- Tapes with three density operation (200, 556, or 800 characters per inch at speeds up to 120,000 characters per second) and backward reading capability
- Dual, triple, or quadruple channel capability on tape controllers with either 604 or 607 tape transports
- 1200 card per minute reader
- 1000 line per minute printer with 136 columns
- 200 million bit capacity disc units with 80,000 cycle transfer rate

The computation section may be functionally divided into an arithmetic and a control section. The control section directs the operations required to execute instructions and establishes the timing relationships for performing operations in proper sequence. It also sends the commands necessary to begin processing input/output data.

Non-numeric instructions are available for logical data handling, test commands for two-way branching, control transfer, character handling, indexing, and storage searching.

ARITHMETIC OPERATION

The arithmetic section performs the arithmetic and logical operations necessary for executing instructions.

Register	No. of Stages	Modulus	Complement Notation	Arithmetic	Result
A	48	$2^{48}-1$	one's	subtractive	signed
Q	48	$2^{48}-1$	one's	-----	signed
P	15	2^{15}	two's	additive	unsigned
Index	15	$2^{15}-1$	one's	-----	-----

Table 1 - Arithmetic Properties of Registers

The arithmetic in a standard 3400 system is in fixed point and in floating point. Examples are shown in Figure 1. In the minimum configuration, arithmetic instructions are in fixed point only. The range of values is:

$$\begin{array}{ll} \text{Fixed Point Number} & \pm 2^{47}-1 \\ \text{Floating Point Number} & \pm 10^{\pm 308} \end{array}$$

INTERRUPT SYSTEM The interrupt system tests for the following internal and external hardware conditions:

- Shift or divide fault
- Overflow
- Underflow
- Channel inactive
- Parity error
- Interrupts
- Memory bound fault
- Clock

Conditions are sampled after each main program instruction is executed; if a condition exists, execution halts and the contents of the program address register are stored. After the necessary action is taken by the program interrupt routine, control returns to the next main program step.

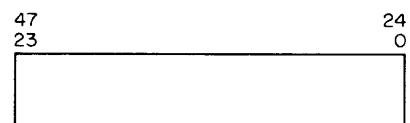
INSTRUCTION FORMAT

A computer word consists of 48 bits and may be interpreted as one 48-bit data word, a 48-bit instruction, or two 24-bit instructions (Figure 1).

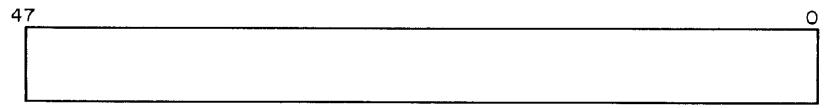
Most of the instructions are 24-bit instructions, common to the CONTROL DATA® 1604 and 3600 computers. These instructions have a 6-bit operation code. They are arranged in one 48-bit word: the higher order 24 bits are the upper instruction; the lower 24 bits are the lower instruction.

Instructions which are not common to the 1604 computer differ in format and in word length (some are 24 bits, others are 48 bits).

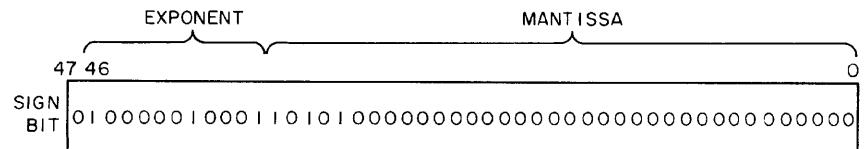
24-bit Instruction



48-bit Instruction



Floating Point Number 250,000



Fixed Point Number 250,000

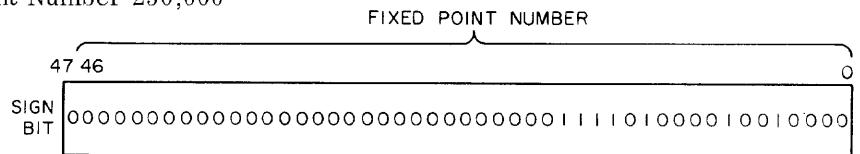


Figure 1. Instruction Formats

Table 2
Machine Language Instructions

<u>Octal Code</u>	<u>Mnemonic Code</u>	<u>Name</u>	<u>Indirect</u>	<u>Address</u>	<u>Modification</u>
<u>Inter-Register Transmission</u>					
	IAQ	Interchange A and Q (00700554)	No		No
<u>Full-Word Transmission</u>					
12	LDA	Load A	Yes		Yes
16	LDQ	Load Q	Yes		Yes
20	STA	Store A	Yes		Yes
21	STQ	Store Q	Yes		Yes
13	LAC	Load A, Complement	Yes		Yes
17	LQC	Load Q, Complement	Yes		Yes
<u>Address Transmission</u>					
61	SAL	Substitute Address (lower)	Yes		Yes
60	SAU	Substitute Address (upper)	Yes		Yes
04	ENQ	Enter Q	Yes		Yes
10	ENA	Enter A	Yes		Yes
53	LIL	Load Index (lower)	Yes		No
52	LIU	Load Index (upper)	Yes		No
57	SIL	Store Index (lower)	Yes		No
56	SIU	Store Index (upper)	Yes		No
50	ENI	Enter Index	Yes		No
	ATI	Transmit A to Index (0074054b)	No		No
<u>Fixed Point Arithmetic</u>					
14	ADD	Add	Yes		Yes
15	SUB	Subtract	Yes		Yes
24	MUI	Multiply Integer	Yes		Yes
25	DVI	Divide Integer	Yes		Yes
<u>Address Arithmetic</u>					
11	INA	Increase A	Yes		Yes
51	INI	Increase Index	Yes		No
54	ISK	Index Skip	Yes		No
<u>Logical</u>					
40	SST	Selective Set	Yes		Yes
41	SCL	Selective Clear	Yes		Yes
42	SCM	Selective Complement	Yes		Yes
43	SSU	Selective Substitute	Yes		Yes
44	LDL	Load Logical	Yes		Yes
45	ADL	Add Logical	Yes		Yes
46	SBL	Subtract Logical	Yes		Yes
47	STL	Store Logical	Yes		Yes
<u>Shifting</u>					
01	ARS	A Right Shift	Yes		Yes
02	QRS	Q Right Shift	Yes		Yes
03	LRS	Long Right Shift (AQ)	Yes		Yes
05	ALS	A Left Shift	Yes		Yes
06	QLS	Q Left Shift	Yes		Yes
07	LLS	Long Left Shift (AQ)	Yes		Yes
34	SCA	Scale A	Yes		No
35	SCQ	Scale AQ	Yes		No

<u>Octal Code</u>	<u>Mnemonic Code</u>	<u>Name</u>	<u>Indirect</u>	<u>Address</u>	<u>Modification</u>
<u>Replace</u>					
70	RAD	Replace Add	Yes		Yes
71	RSB	Replace Subtract	Yes		Yes
72	RAO	Replace Add One	Yes		Yes
73	RSO	Replace Subtract One	Yes		Yes
<u>Storage Test</u>					
36	SSK	Storage Skip	Yes		Yes
37	SSH	Storage Shift	Yes		Yes
<u>Search</u>					
64	EQS	Equality Search	Yes		Yes
65	THS	Threshold Search	Yes		Yes
66	MEQ	Masked Equality Search	Yes		Yes
67	MTH	Masked Threshold Search	Yes		Yes
<u>Jumps and Stops</u>					
22	AJP	A Jump	No		No
23	QJP	Q Jump	No		No
55	IJP	Index Jump	Yes		No
75	SLJ	Selective Jump	No		No
76	SLS	Selective Stop	No		No
<u>Input/Output</u>					
74.0	CONN	Connect	No		No
74.1	EXTF	Function	No		No
74.2	BEGR	Read	No		No
74.3	BEGW	Write	No		No
74.4	COPY	Copy Status	No		No
74.5	CLCH	Clear Channel	No		No
74.6	CCWD	Change Control Word	No		No
77.2	CIS	Copy Interrupt Status	No		No
77.3	SEN	Internal Sense	No		No
77.4	CPR*	Copy Product Register	No		No
77.0	INF	Internal Function	No		No
<u>Miscellaneous</u>					
77.1	AUG	Augment	Yes		Yes
77.5	EUB	Enter Upper Bound	No		No
77.6	ELB	Enter Lower Bound	No		No
	LDC	Load Character (63bv0006500m)	Yes		Yes
	STC	Store Character (63bv0006505m)	Yes		Yes
<u>Illegal Codes</u>					
26					
27					
74.7	Return Jump to Address 00020				
77.7					
<u>Single Precision Floating Point Arithmetic</u> (floating point option)					
30	FAD	Floating Add	Yes		Yes
31	FSB	Floating Subtract	Yes		Yes
32	FMU	Floating Multiply	Yes		Yes
33	FDV	Floating Divide	Yes		Yes

Table 3
3400 Instruction Times

<u>Octal Code</u>	<u>Mnemonic Code</u>	<u>Name</u>	<u>Execution in usec</u>
24	MU I	Multiply Integer	22.5
25	DV I	Divide Integer	22.5
01-03	All Shifts		3.0+0.2 (K-2)
05-07			(K is modified shift count - for K less than 3, T = 3.0 usec).
34-35	Scale A and Scale AQ		3.0+0.2 (P-2) (P is number of places shifted).
70	RAD	Replace Add	
71	RSB	Replace Subtract	
72	RAD	Replace Add One	
73	RSO	Replace Subtract One	
37	SSH	Storage Shift	6.1
64	EQS	Equality Search	
65	THS	Threshold Search	
66	MEQ	Masked Equality Search	
67	MTH	Masked Threshold Search	
22	AJP	A Jump	upper, jump taken 2.2
23	QJP	Q Jump	upper, jump not taken 3.0
55	IJP	Index Jump	lower, jump taken 1.4
75	SLJ	Selective Jump	lower, jump not taken 3.0
76	SLS	Selective Stop	
63.0	UBJP	Unconditional Jump	3.8 no jump 2.2 jump made
63.0	BRTJ	Unconditional Return Jump	3.8 no jump 3.8 jump made
63.1	BJPL	Unconditional Jump to Lower	3.8 no jump 3.0 jump made
74.0	CONN	Connect	2.2 if rejected by channel active.
74.1	EXTF	Function	5.8 and up depending on external equipment. Up to a maximum of 100 usec if the time reject is taken.
74.2	BEGR	Read	2.2 reject
74.3	BEGW	Write	3.8 no reject
74.4	COPY	Copy Status	3.8
74.5	CLCH	Clear Channel	100
74.6	CCWD	Change Control Word	2.2 reject
63bv0006500m	LDCH	Load Character	3.8
63bv0006505m	STCH	Store Character	6.1
Illegal Codes			3.7 lower 4.5 upper
30	FAD	Floating Add	6.8*
31	FSB	Floating Subtract	6.8*
32	FMU	Floating Multiply	19.8**
33	FDV	Floating Divide	19.8**
All other legal instructions			3.0

* For equal exponents and no normalizing

** No normalizing

For each shift to equalize exponents or to normalize, add 0.2 usec

SYSTEM**CONFIGURATION** Basic Configuration:

Central Processor (3404)

Control Console (3401)

Standard Input/Output Channel (3406)

This configuration which lacks input/output devices is suitable as a control computer. The standard 3400 software cannot be used with the basic configuration.

Standard Configuration:

On-Line -- An on-line system which permits use of standard 3400 software consists of the equipment listed in table 4.

The recommended list of on-line equipment differs from the minimum list in three particulars: (1) another unit of the same model is added, (2) the model is changed, (3) the model and the number of units are changed.

Minimum Configuration	Recommended Configuration
One Central Processor (3404) with 32K Memory (3409)	
One Standard Input/Output Channel (3406)	Two Standard Input/Output Channels (3406)
One Floating Point Option (3410)	
One Control Console (3401)	
One Magnetic Tape Controller (3228)	One Magnetic Tape Controller (3422)
Four Magnetic Tape Transports (604)	Six Magnetic Tape Transports (607)
One Line Printer Controller (3256)	
One Line Printer (501)	
One Card Punch Controller (3245)	
One Card Punch (IBM 523)	
One Card Reader Controller (3248)	
One Card Reader (405)	

Table 4 - On-Line Standard Software Compatible Configuration

All equipment which may be used in the 3400 Computer System is shown in figure 2. It does not represent any recommended configuration.

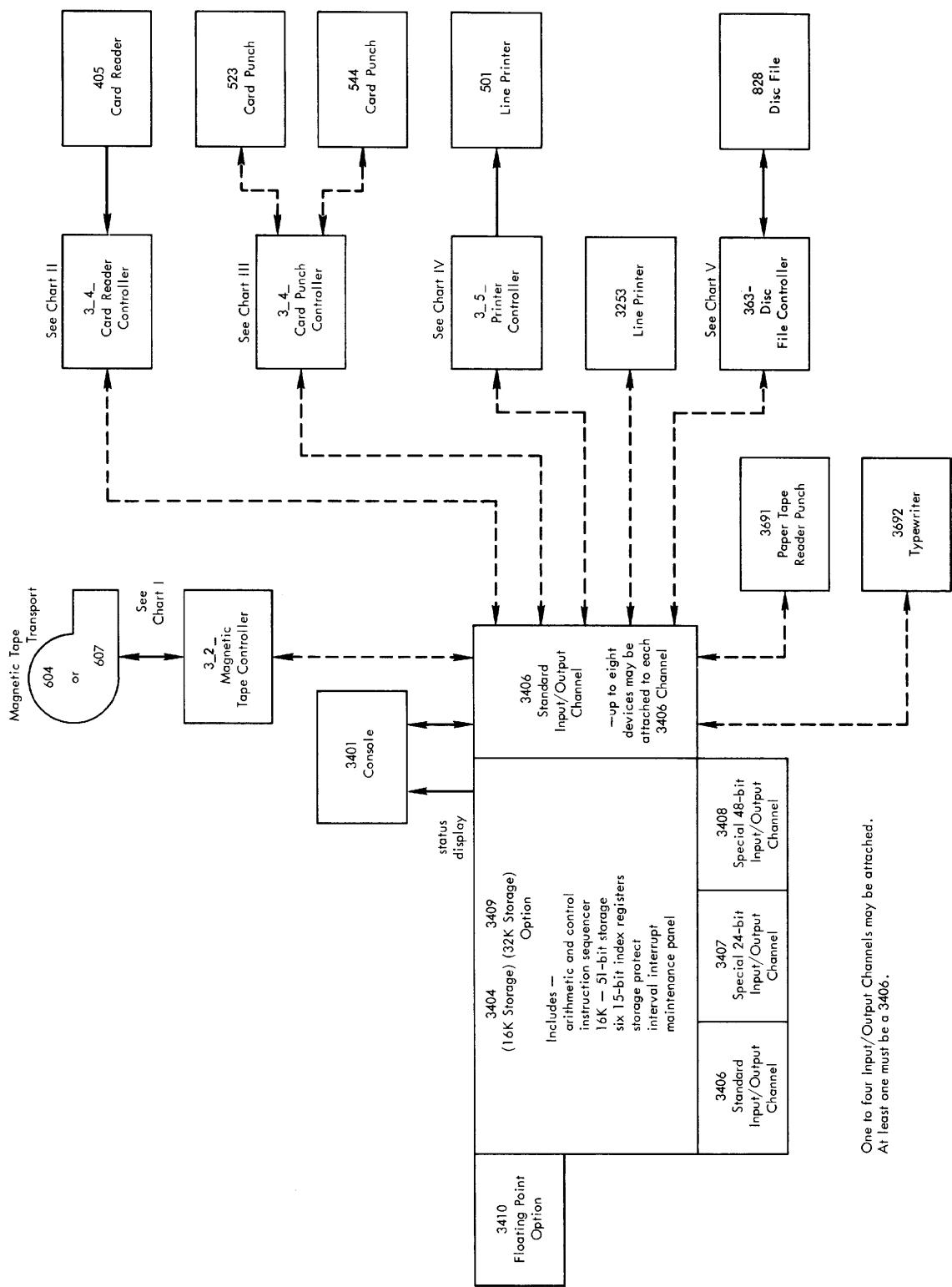


Figure 2 - Block Diagram Relating all 3400 Equipment

Chart I - Magnetic Tape Controllers

Model No.	No. of Channels	Max. No. of Tapes
3228	1	4
3229	1	8
3421	2	4
3422	2	6
3423	2	8
3622	2	16
3625	3	8
3626	3	16
3623	4	8
3624	4	16

Chart II - Card Reader Controllers

Model No.	No. of Channels	No. of Core Buffer
3248	1	No
3447	1	Yes
3649	2	Yes

Chart IV-Line Printer Controllers

Model No.	No. of Channels	Checking
3256	1	No
3257	1	Yes
3659	2	No

Chart V-Disc File Controllers

Model No.	No. of Channels	No. of Discs
3632	2	1
3633	2	2
3634	2	3
3635	2	4

Off-Line -- An off-line system which permits use of standard 3400 software consists of the equipment listed in table 5. The recommended list of off-line equipment differs from the minimum list by addition of units and by changes in the unit model.

Minimum Configuration	Recommended Configuration
One Central Processor (3404) with 32K Memory (3409)	
One Standard Input/Output Channel (3406)	Two Standard Input/Output Channels (3406)
One Floating Point Option (3410)	
One Control Console (3401)	
One Magnetic Tape Controller (3228)	One Magnetic Tape Controller (3625)
Six Magnetic Tape Transports (604)	Eight Magnetic Tape Transports (607)
One Computer (160A)	
One Line Printer Controller (3256)	One Line Printer Controller (3659)
One Line Printer (501)	
One Card Punch Controller (3245)	One Card Punch Controller (3644)
One Card Punch (IBM 523)	
One Card Reader Controller (3248)	One Card Reader Controller (3649)
One Card Reader (405)	
One Data Channel Converter (3681)	

Table 5 - Off-Line Standard Software Compatible Configuration

COMPONENT DESCRIPTION

Central Processor (3404, 3409) -- The central processor is a solid state computer which performs calculations and processes data in parallel binary mode through the step-by-step execution of individual instructions. More than 250,000 commands are executed per second. The standard 3400 magnetic core storage section provides high speed, random-access storage for 32,768 words. The basic 3400 magnetic core storage section provides storage for 16,384 words (the 3409 is removed in this case).

Control Console (3401) -- The console provides the communication link between computer system, stored program, and operator. It consists of a system status display panel, speaker, electric typewriter, and various control switches. A chair is included.

Standard Input/Output Channel (3406) -- One input/output channel may control a maximum of eight external equipments.

Special Input/Output Channels -- Two special input/output channel units are available. The 3407 is used with peripheral equipment handling 24 bits or less. The 3408 is used with peripheral equipment handling 48 bits or less.

Floating Point Option (3410) -- This option adds four single-precision floating point instructions to the machine language.

Magnetic Tape Controller -- The ten available magnetic tape controllers are listed below. Each controller unit permits independent reading and writing on the tape it controls.

Model No.	No. of Channels	Max. No. of Tapes
3228	1	4
3229	1	8
3421	2	4
3422	2	6
3423	2	8
3622	2	16
3625	3	8
3626	3	16
3623	4	8
3624	4	16

Magnetic Tape Transport -- Two magnetic tape transport units are available. The 604 which moves tape 75 inches per second is available in densities of 200 and 556 characters per inch at rates of 66 and 24 microseconds per frame, respectively. The 607 which moves tape 150 inches per second is available in densities of 200, 556, and 800 characters per inch at rates of 33, 12, and 8 microseconds per frame, respectively.

Card Reader Controller -- Three card reader controller units are available. The 3248 connects a 405 card reader to an input/output channel or data channel converter without buffering. The 3447 performs the same

function with buffering. The 3649 connects a 405 card reader to an input/output channel with buffering.

Card Punch Controller -- Three card punch controller units are available. The 3245 connects an IBM 523 or 544 card punch to an input/output channel or to a data channel converter without buffering. The 3446 performs the same function with buffering. The 3644 connects a card punch to two input/output channels or data channel converters with buffering.

Line Printer Controller -- Three line printer controller units are available. The 3256 connects a 501 high speed line printer to an input/output channel or data channel converter without checking. The 3257 performs the same function with checking. The 3659 connects a 501 to two input/output channels or data channel converters.

High Speed Line Printer (501) -- The capacity of this printer is 136 columns at 1000 lines per minute with a character set of 64 symbols.

Line Printer (3253) -- The capacity of this printer is 120 columns at 300 lines per minute with a character set of 64 symbols.

Disc File Controller -- Each of the four disc file controllers permits two independent reading, writing or positioning functions. The difference is in the number of disc files which may be controlled as shown below:

Model No.	Max. No. of Disc Files
3632	1
3633	2
3634	3
3635	4

Disc File (828) -- This file allows simultaneous operation of any two functions -- reading, writing, and positioning. It has a 200 million bit capacity, 80 kc average character transfer rate, and 225 millisecond average access time.

Paper Tape Reader/Punch (3691) -- This unit reads 350 characters per second and punches 110 characters per second.

Program Controlled Input/Output Typewriter (3692) -- This unit operates from a single input/output channel.

STANDARD CONTROL DATA 3400 SOFTWARE

The standard 3400 software is compatible with the standard CONTROL DATA 3600 software. Programs written for the 3400 FORTRAN and COBOL compilers and the COMPASS assembler may be compiled or assembled on the corresponding 3600 compiler or assembly system.

3400 SCOPE

The 3400 SCOPE (Supervisory Control of Program Execution) is a monitor system, complete with library. By controlling the input and output operations, execution of programs, and use of libraries, 3400 SCOPE simplifies coding. It minimizes operating responsibilities, makes maximum use of computer time, and provides a maximum amount of available storage area for job execution. 3400 SCOPE is compatible with 3600 SCOPE.

3400 SCOPE provides the following functions:

- Job processing
 - maintains accounting information
 - initiates compilations and assemblies
 - assigns equipment
 - loads and links subprograms
 - allocates memory
 - provides overlay processing

- Debugging aids
 - SNAP dump
 - error dumps (recovery dumps)
 - octal corrections
 - memory map
 - diagnostics

- Program requests
 - input/output control
 - external interrupt control
 - tape handling

internal interrupt control

sampling of time, date, equipment status, and available memory

Library preparations and maintenance

listing the table of contents

directory

preparing and editing library tape

3400 SCOPE library contains:

3400 COMPASS

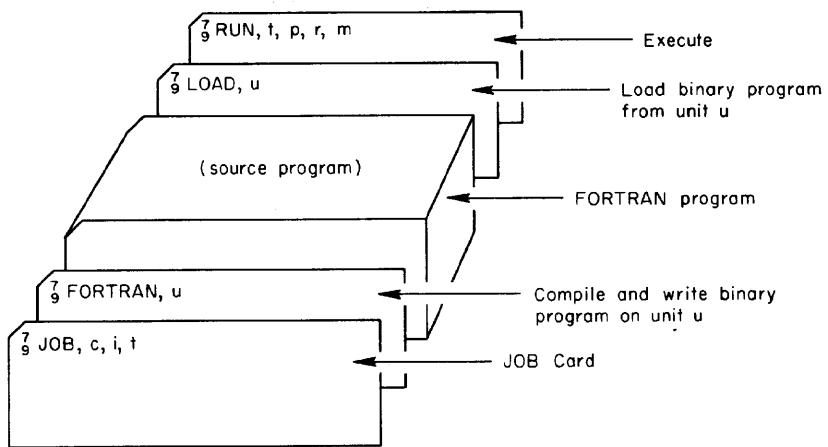
3400 FORTRAN

3400 COBOL

MACRO Library

SUBROUTINE Library

Compile a FORTRAN source program on unit u. Load and execute the program.



Assemble a COMPASS subprogram on unit u. Load with a binary subprogram and execute.

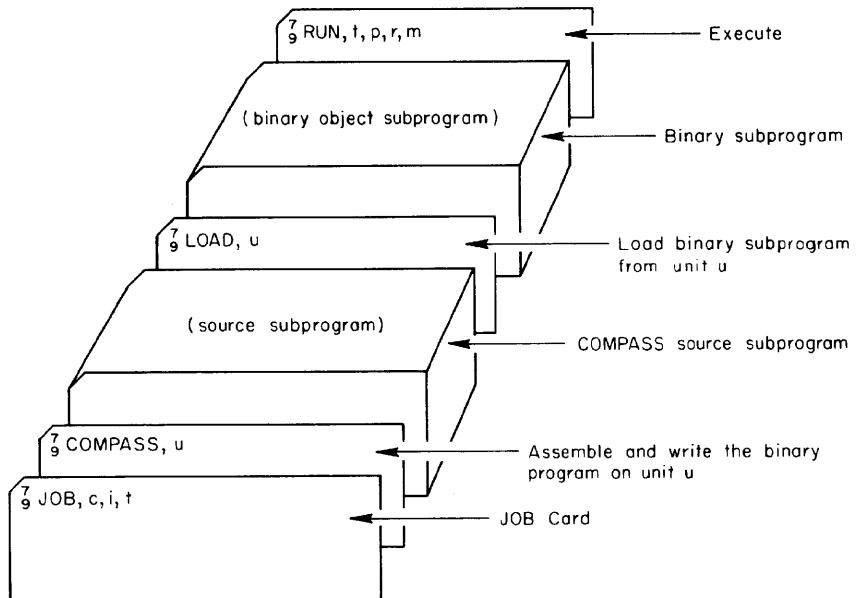


Figure 3 - 3400 SCOPE Use

3400 COMPASS

3400 COMPASS is a comprehensive assembly system. The instructions loaded in the central processor storage are in binary. Writing a machine language program requires considerable attention to detail. To simplify coding, 3400 COMPASS provides the programmer with a symbolic coding language which is converted by COMPASS into a form acceptable to the central processor. Source programs may consist of punched cards or binary-coded decimal card images on magnetic tape. Several forms of output are available from the assembler, including an assembly listing, a relocatable binary object program on punched cards or magnetic tape, and a Compressed Symbolic (COSY) source card deck.

3400 COMPASS contains many features of 3600 COMPASS and is compatible with it. The language includes:

Address Arithmetic	Constants, symbolic address, and arithmetic expressions may be used for addresses.
Preloaded Data	Data areas may be specified and loaded with data in the source program.
Common Assignments	Common areas may be designated to facilitate communication between sub-programs.
Data Definitions	Integer, floating point, binary-coded decimal, and typewriter constants may be designated.
Library Routine Calls	Library routines, input/output routines, and macros may be called.
Listing Control	The vertical format of the assembly listing may be controlled with COMPASS pseudo instructions.
Diagnostics	Diagnostics for source program errors are included with the output listing.
Macro Instructions	Sequences of instructions from the macro library or previously defined by a macro statement will be inserted by the assembler when the macro name appears in an operation field.

COMPASS CODING FORM

PROGRAM AREA COMPUTATION

ROUTINE

LOCATION	OPERATION, MODIFIERS	ADDRESS FIELD	CONTROL DATA		NAME PAGE	
			CORPORATION	DATE		
COMMENTS						
1.1.2.1.4.5.1.6.7.8.9.10.11.12.1.3.1.4.1.5.1.6.1.7.1.8.1.9.1.20.1.21.1.22.1.23.1.24.1.25.1.26.1.27.1.28.1.29.1.30.1.31.1.32.1.33.1.34.1.35.1.36.1.37.1.38.1.39.1.40.1.41.1.42.1.43.1.44.1.45.1.46.1.47.1.48.1.49.1.50.1.51.1.52.1.53.1.54.1.55.1.56.1.57.1.58.1.59.1.60.1.61.1.62.1.63.1.64.1.65.1.66.1.67.1.68.1.69.1.70.1.71.1.72.1.73.1.74.1.75.1.76.1.77.1.78.1.79.1.80						
1 IDENT	AR.EA.	1	NDB	0.1		
1 SPACE			NDB	0.2		
1 REM	THIS SUBPROGRAM COMPUTES THE AREA OF A CIRCLE		NDB	0.3		
1 REM	GIVEN THE VALUE OF THE RADIUS, IN FLOATING POINT		NDB	0.4		
1 REM	FORM, IN THE ACCUMULATOR, AND EXITS WITH THE AREA		NDB	0.5		
1 REM	IN THE ACCUMULATOR.		NDB	0.6		
1 SPACE	1		NDB	0.7		
1 AREA	SLJ * *		NDB	0.8		
1 STQ	TEMP + 1	SAVE Q REGISTER	NDB	0.9		
1 STA	TEMP		NDB	1.0		
1 FMU	TEMP	AREA = PI * R * R	NDB	1.1		
1 FMU	= D3.14159265	RESTORE Q REGISTER	NDB	1.2		
1 LDQ	TEMP + 1	EXIT	NDB	1.3		
1 SLJ	AREA		NDB	1.4		
1 SPACE	1		NDB	1.5		
1 TEMP	BSS 2	WORKING STORAGE	NDB	1.6		
1 SPACE	1		NDB	1.7		
1 ENTRY	AREA		NDB	1.8		
1 SPACE	1		NDB	1.9		
1 END						
1.1.2.1.4.5.1.6.7.8.9.1.12.1.5.1.6.1.7.1.8.1.9.1.10.1.11.1.12.1.13.1.14.1.15.1.16.1.17.1.18.1.19.1.20.1.21.1.22.1.23.1.24.1.25.1.26.1.27.1.28.1.29.1.30.1.31.1.32.1.33.1.34.1.35.1.36.1.37.1.38.1.39.1.40.1.41.1.42.1.43.1.44.1.45.1.46.1.47.1.48.1.49.1.50.1.51.1.52.1.53.1.54.1.55.1.56.1.57.1.58.1.59.1.60.1.61.1.62.1.63.1.64.1.65.1.66.1.67.1.68.1.69.1.70.1.71.1.72.1.73.1.74.1.75.1.76.1.77.1.78.1.79.1.80						
10 : FRC	ALPHA 1	UNF	ALPHA 2	2 : TWO		
10 : FRC						

3400 FORTRAN

In the 3400 FORTRAN programming language, mathematical or scientific problems may be expressed in algebraic notation. 3400 FORTRAN incorporates elements present in other FORTRAN systems as well as features unique to this system.

The 3400 FORTRAN language includes:

Constants and variables of the following types:

Integer	Single precision floating point
Octal	Double precision floating point
Hollerith	Complex floating point
Logical	

Programmer-defined variable types

Mixed mode arithmetic

Multiple arithmetic replacement statements

Masking logical (Boolean) and relational operators

Library functions

Independently compilable subprograms

Multiple entry points to functions and subroutines

Variable dimension specification

Variable format for input/output control

Conversion formats: double precision, complex, right justifying
binary-coded decimal, and logical.

Buffering of all input/output operations

Memory-to-memory data transmission operations

Multi-branch status statement

FORTRAN CODING FORM

PROGRAM		CONTROL DATA		NAME	
ROUTINE		CORPORATION		DATE	
STATEMENT		FORTRAN STATEMENT		PAGE OF	
				SERIAL NUMBER	
STATE- MEN- T C- O- P- E- N- T- E- NO.		I = ONE I = ALPHA I		2 = TWO Z = ALPHA Z	
FUNCTION CTPC((Z,W))					
TYPE(CMPLEX(Z,W),I,C) = CTPC(Z,W)					
TYPE REAL, L, G, R					
DATA ((P1=1.0,7.963268),(I=0,1.0))					
A=Z, \$=-B-I*Z, \$=W, D=-I*Z,					
IF(A)20,10					
P=B, \$=THETA=P1, \$=G# T# 30					
THETA=ATAN(B/A)					
R=SQR(TF((A,A+B,B)))					
L#GR=L#GF(R)					
CT#C=(-1,EXPF((C*L#GR,-D*THETA)))*(C#SF((D*L#GR)+C#THETA))					
* SIN((D*L#GR+I)*I)					
RETURN					
END					
21					

3400 COBOL

3400 COBOL is a programming language specifically designed to permit business and data processing problems to be defined and solved using English language notation. In processing data, two elements are involved: a set of procedures which specifies how data is to be manipulated, and a description of the data involved.

3400 COBOL consists of four divisions. The Identification Division identifies the source program and the outputs of a compilation. The Environment Division specifies the particulars of the 3400 Computer System. The Data Division describes the files of data the object program is to manipulate or create and the individual logical records which comprise these files. The Procedure Division specifies the steps the computer is to follow. These steps are expressed in English words, statements, sentences, and paragraphs. Verbs denote actions, sentences describe procedures, and conditional statements provide alternative paths of action.

3400 COBOL, which includes all required COBOL, and many desirable elements of elective COBOL, contains the following features:

- Operates under 3400 SCOPE
- Upward compatible with 3600 COBOL
- Label checking
- Alphanumeric handling
- Fixed and variable length records

COBOL CODING FORM		NAME:	PAGE	0f
CONTROL DATA CORPORATION		DATE:	IDENT:	13
SEQ. NO.	C A B	STATEMENTS		
00001	I D E N T I F I C A T I O N D I V I S I O N .			
00002	P R O G R A M - I D . E X A M P L E -	P R O G R A M .		
00003	E N V I R O 			
00004	C O N F I G 			
00005	S O U R C E 	C O M P U T E R .3 4 0 0 .		
00006	O B J E C T -	C O M P U T E R .3 4 0 0 .		
00007	S P E C I A L -	N A M E S .	P R I N T .	
00008	D A T A -	D I V I S I O N .		
00009	W O R K I N 	S T O R A G E S E C T I O N .		
00010	0 7 7 .	A ,	P I C T U R E I S X (5 	*
00011	0 1 .	P A R A M E T R S .		
00012	0 0 1 2 .	0 2 .	N U M B E R S .	
00013	0 0 1 3 .	0 3 .	B ,	P I C T U R E I S S 9 (
00014	0 0 1 4 .	P R O C E D U R E .	D I V I S I O N .	
00015	0 0 1 5 .	S T A R T -	R U N .	D I S P L A Y # 1 .
00016	0 0 1 6 .	S T A R T -	T E S T .	T E S T S O N C O N D I T I ON AL S T A T M
00017	0 0 1 7 .	P R O C E D U R E .	P R O C E D U R E - B L O 	
00018	0 0 1 8 .	S T O P .	R U N .	
00019	0 0 1 9 .	P R O C E D U R E - B L O C K 	R E L A T IO N AL C O N 	
00020	0 0 2 0 .	M O V E .	# T E S T .	S I M P L E
00021	0 0 2 1 .	M O V E .	TO Z .	
00022	0 0 2 2 .	M O V E 1 .	TO B .	

SORT/MERGE

The 3400 SORT/MERGE program organizes data on magnetic tape into one continuous predetermined order. SORT/MERGE is included in the 3400 SCOPE library and operates under the SCOPE monitor system. It may be used as a subroutine in an assembly program or as a single program using control cards.

SORT/MERGE includes the following features:

- Operates under SCOPE
- Binary coded decimal and binary files
- Varied collating sequences
- Fixed and variable length records
- Merge presorted input files with sorted output
- Merge only presorted input files
- Exits for user routines
- Label checking
- Recovery protection

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